



Arka Educational & Cultural Trust (Regd.)

JAIN INSTITUTE OF TECHNOLOGY, DAVANGERE

(A Unit of Jain Group of Institutions, Bangalore)



Department of Electronics & Communication Engineering.



Encore

2024-2025 Technical Magazine



"The fundamental problem of communication is that of reproducing at one point either exactly or approximately a message selected at another point."

- Claude Shannon

About The Department



Dr. Santosh Herur,
HOD, Dept of ECE,
JIT, Davangere.

Department of Electronics and Communication was started in the year 2011-12. The department emphasizes on imparting advanced technical knowledge in various fields like signal processing, VLSI Technology, Embedded system, wireless technology, microwave engineering, optical communication, and microelectronics and control systems. Highly qualified, experienced and dedicated staff members are the backbone of the department. Well equipped, sophisticated, precision and state of the art laboratories are center of attraction in the campus. Another major strength of the department is its extensive collection of technical books, periodical and journals, ENCORE association of the department of E & CE aims at targeting innovations in the field of advanced communication and networking.

The Department has MOU with Karunadu Technologies Pvt. Ltd, Bengaluru, Abiyantrix, Soft Solutions, Davangere, Sai Tech, Mysore. The MOU organizations provide Internship training and conduct workshops on recent trends and organize seminars for the students to enhance their industrial exposure.

Editor's desk



Ms. NAYANA T
Assistant Professor
Dept. of ECE,
JIT, Davangere

*Warm greetings to the readers of **Encore!!***

We are pleased to share with you the Technical Magazine for the Academic year 2024-25. We express our considerable gratitude and appreciation to everyone who have contributed to this Newsletter magazine. These contributions have required a generous amount of time and effort. It is this willingness to share knowledge, concerns and special insights with fellow beings that has made this magazine possible.

Finally, we would like to conclude with these words "Learn from yesterday, live for today, hope for tomorrow. the important thing is not to stop questioning."- Albert Einstein Embrace each challenge in your life as an opportunity for self-transformation.

Happy reading

Thank you

NAYANA. T

Vision of the Department

Endeavours to achieve academic excellence by providing value based education to meet the ever changing needs of the society.

Mission of the Department

- ❖ *To emphasize on student focus learning environment.*
- ❖ *To prepare graduates who surpass in the engineering career, qualified to acquire technical knowledge and ethical values.*
- ❖ *To provide a platform to inculcate team spirit and leadership qualities to succeed in professional Career.*

Program Specific Outcomes (PSOs)

- ❖ *PSO1: Analyze, simulate and design electronic and communication circuits for given specifications.*
- ❖ *PSO2: Design and test processor based systems*

Program Educational Objectives (PEOs)

- ❖ *PEO1: To create graduates with a strong base in Electronics & Communication Engineering.*
- ❖ *PEO2: To produce technically competent graduates with the ability to understand, analyze & develop electronic systems.*
- ❖ *PEO3: Ability to work with professional ethics and as a team member to realize the goals of the projects.*
- ❖ *PEO4: Excellence in professional career by attaining higher education.*

Institute Academic Calendar of Events

ODD SEMESTER



ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davanagere.

ACADEMIC CALENDAR

III SEMESTER B.E. 2024-25 (AUG 2024 – DEC 2024)

Commencement of the Semester: 19/08/2024

Last working day: 21/12/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
AUGUST 2024	19	20	21	22	23	24	25	19 th - Commencement of Classes	
	26	27	28	29	30	31			
SEPTEMBER 2024	2	3	4	5	6	7	8	15 th - Engineer's day	7 th - Ganesha Chaturthi 16 th - Eid-Milad
	9	10	11	12	13	14	15		
	16	17	18	19	20	21	22		
	23	24	25	26	27	28	29		
	30								
OCTOBER 2024		1	2	3	4	5	6	1 st - Internal Assessment 21 st , 22 nd & 23 rd	2 nd - Gandhi Jayanthi 11 th - Aayudha pooja 12 th - Vijayadashami 17 th - Valmiki Jayanthi 31 st - Naraka Chaturdashi
	7	8	9	10	11	12	13		
	14	15	16	17	18	19	20		
	21	22	23	24	25	26	27		
	28	29	30	31					
NOVEMBER 2024					1	2	3	9 th - Parents meeting	1 st - Kannada Rajyotsava 2 nd - Balipadyami 18 th - Kanakadasa Jayanthi
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30			
DECEMBER 2024	2	3	4	5	6	7	8	2 nd Internal Assessment 16 th , 17 th & 18 th Lab IA : 19 th to 21 st	
	9	10	11	12	13	14	15		
	16	17	18	19	20	21			

Total No. of Working days : 98

VTU ODD Semester Exams

Practical Exams	23-12-2024 To 03-01-2025	Theory Exams	06-01-2025 To 04-02-2025
	Holidays		Parents Meeting
	Internal Assessment Tests		

8/8/24

Co-ordinator
(Dr. Nataraja G)

Dean Academics
(Dr. Madhukeshwara N)

IQAC Director
(Dr. Santosh Herur)

Principal
(Dr. Ganesh D B)

Institute Academic Calendar of Events

ODD SEMESTER



ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davanagere.

ACADEMIC CALENDAR

V SEMESTER B.E. 2024-25 (SEPT 2024 – DEC 2024)

Commencement of the Semester: 17/09/2024

Last working day: 21/12/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
SEPTEMBER 2024		17	18	19	20	21	22	17 th -Commencement of the Classes	
	23	24	25	26	27	28	29		
	30								
OCTOBER 2024		1	2	3	4	5	6		2 nd - Gandhi Jayanthi 11 th - Mahanavami 12 th - Vijaydashami 17 th -Valmiki Jayanthi 31 st -Narakachaturdashi
	7	8	9	10	11	12	13		
	14	15	16	17	18	19	20		
	21	22	23	24	25	26	27		
	28	29	30	31					
NOVEMBER 2024					1	2	3	1 st - Internal Assessment 4 th ,5 th ,6 th &7 th 16 th -Parents meet	1 st - Kannada Rajyotsava 2 nd -Balipadyami 18 th -Kanakadasa Jayanthi
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30			
DECEMBER 2024							1	Lab IA-12 th to 14 th 2 nd -Internal Assessment 16 th ,17 th ,18 th &19 th	
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15		
	16	17	18	19	20	21			

Total No. of Working days : 75

VTU ODD Semester Exams

Practical Exams	23-12-2024 To 03-01-2025	Theory Exams	06-01-2025 To 04-02-2025
	Holidays		Parents Meeting
	Internal Assessment Tests		

Co-ordinator
(Dr. Nataraja G)

Dean Academics
(Dr. Madhukeshwara N)

IQAC Director
(Dr. Santosh Herur)

Principal
(Dr. Ganesh D B) 14/09/24

Institute Academic Calendar of Events

ODD SEMESTER



ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davanagere.

ACADEMIC CALENDAR

VII SEMESTER B.E. 2024-25 (SEPT 2024 – DEC 2024)

Commencement of the Semester: 09/09/2024

Last working day: 21/12/2024

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
SEPTEMBER 2024	9	10	11	12	13	14	15	9 th - Commencement of the Classes	
	16	17	18	19	20	21	22		
	23	24	25	26	27	28	29		
	30								
OCTOBER 2024		1	2	3	4	5	6	1 st - Internal Assessment 14 th , 15 th , 16 th	2 nd - Gandhi Jayanthi 11 th - Mahanavami 12 th - Vijaydashami 17 th - Valmiki Jayanthi 31 st - Narakachaturdashi
	7	8	9	10	11	12	13		
	14	15	16	17	18	19	20		
	21	22	23	24	25	26	27		
	28	29	30	31					
NOVEMBER 2024					1	2	3	16 th - Parents meet 2 nd - Internal Assessment 21 st , 22 nd , 23 rd	1 st - Kannada Rajyotsava 2 nd - Balipadyami 18 th - Kanakadasa Jayanthi
	4	5	6	7	8	9	10		
	11	12	13	14	15	16	17		
	18	19	20	21	22	23	24		
	25	26	27	28	29	30			
DECEMBER 2024							1	Lab 1A-12 th to 14 th 3 rd - Internal Assessment 16 th , 17 th , 18 th	
	2	3	4	5	6	7	8		
	9	10	11	12	13	14	15		
	16	17	18	19	20	21			

Total No. of Working days : 82

VTU ODD Semester Exams

Practical Exams	23-12-2024 To 03-01-2025	Theory Exams	06-01-2025 To 04-02-2025
	Holidays		Parents Meeting
	Internal Assessment Tests		

Co-ordinator
(Dr. Nataraja G)

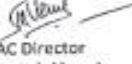
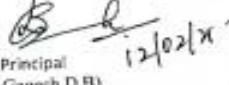
Dean Academics
(Dr. Madhukeshwara N)

IQAC Director
(Dr. Santosh Herur)

Principal
(Dr. Ganesh D B)

Institute Academic Calendar of Events

EVEN SEMESTER

		ARKA Educational & Cultural Trust (Regd.)							
Jain Institute of Technology, Davangere									
# 323, Near Veereshwara Paryashrama, Awaragere, Davangere- 577003.									
ACADEMIC CALENDAR									
IV & VI SEMESTER B.E. 2024-25 (FEB 2025 – MAY 2025)									
Commencement of IV & VI Semester: 10/02/2025				Last working day: 31/05/2025					
MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
FEBRUARY	10	11	12	13	14	15	16	10 th Commencement of Classes 28 th National Science day	26 th Mahashivaratri
	17	18	19	20	21	22	23		
	24	25	26	27	28				
MARCH						1	2	26 th Technical Fest 27 th Sports Day 28 th Shreunga	31 st Quteh-e-ramzan
	3	4	5	6	7	8	9		
	10	11	12	13	14	15	16		
	17	18	19	20	21	22	23		
	24	25	26	27	28	29	30		
APRIL	31							1 st Internal Assessment 7 th , 8 th , 9 th 12 th Parents meet	10 th Mahaveera jayanthi 14 th Ambedkar jayanthi 18 th Good Friday 30 th Basava jayanthi
		1	2	3	4	5	6		
	7	8	9	10	11	12	13		
	14	15	16	17	18	19	20		
	21	22	23	24	25	26	27		
MAY	28	29	30					Lab Internal Assessment 26 th to 28 th 2 nd Internal Assessment 29 th , 30 th , 31 st	1 st Labour's day
				1	2	3	4		
	5	6	7	8	9	10	11		
	12	13	14	15	16	17	18		
	19	20	21	22	23	24	25		
	26	27	28	29	30	31			
Total No. of Working days : 89									
VTU EVEN Semester Exams									
Practical Exams for IV Sem		02-06-2025 To 13-06-2025			Theory Exams		16-06-2025 To 01-08-2025		
Practical Exams for VI Sem		02-06-2025 To 12-06-2025			Theory Exams		16-06-2025 To 01-08-2025		
		Holidays					Parents Meeting		
		Internal Assessment Tests							
 Co-ordinator (Dr. Nataraja G)		 Dean Academics (Dr. Madhukeshwara N)		 IQAC Director (Dr. Santosh Herur)		 Principal (Dr. Ganesh D B)			

Institute Academic Calendar of Events

EVEN SEMESTER



ARKA Educational & Cultural Trust (Regd.)
Jain Institute of Technology, Davangere
 # 323, Near Veereshwara Punyashrama, Avarangere, Davangere- 577003.



ACADEMIC CALENDAR

VIII SEMESTER B.E. 2024-25 (FEB 2025 – MAY 2025)

Commencement of the Semester: 15/02/2025

Last working day: 15/05/2025

MONTH	MON	TUE	WED	THU	FRI	SAT	SUN	EVENTS	HOLIDAYS
FEBRUARY						15	16	15 th Commencement of Classes 28 th National Science day	26 th Mahashivaratri
	17	18	19	20	21	22	23		
	24	25	26	27	28				
MARCH						1	2	26 th Technical Fest 27 th Sports Day 28 th Shrunga	31 st Quteb-e-ramzan
	3	4	5	6	7	8	9		
	10	11	12	13	14	15	16		
	17	18	19	20	21	22	23		
	24	25	26	27	28	29	30		
	31								
APRIL		1	2	3	4	5	6		10 th Mahaveera jayanthi 14 th Ambedkar jayanthi 18 th Good Friday 30 th Basava jayanthi
	7	8	9	10	11	12	13		
	14	15	16	17	18	19	20		
	21	22	23	24	25	26	27		
	28	29	30						
MAY				1	2	3	4	3 rd Farewell Internal Assessment, Seminar, Internship, NCMC	1 st Labour's day
	5	6	7	8	9	10	11		
	12	13	14	15					

Total No. of Working days : 70

VTU ODD Semester Exams

Practical Exams	27-05-2025 To 02-06-2025	Theory Exams	16-05-2025 To 26-05-2025
	Holidays		Parents Meeting
	Internal Assessment Tests		

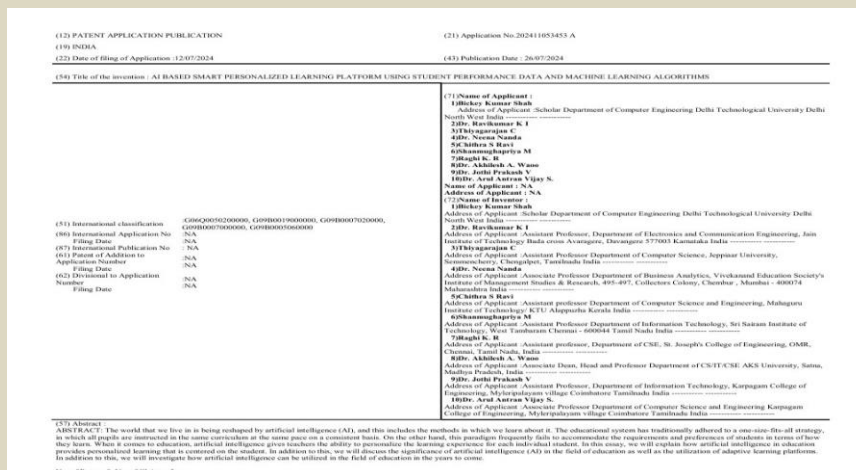
Co-ordinator
 (Dr. Nataraja G)

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Faculty Achievement : Patent Published



Dr. Ravikumar K I, Assistant Professor, Department of E&CE has successfully published a Patent Titled “ AI Based Smart Personalized Learning Platform using Student Performance Data and Machine Learning Alogorithms ”. Hearty Congratulations and Best wishes on your Achievement from College Management, Principal, Advisor, HOD and all the Teaching & Non-teaching staff members of the department.

SEMICON INDIA 2024 : Video Conference





Images of video conference

India is becoming a key player in the global semiconductor industry. In a significant move to further this progress, the **India Semiconductor Mission (ISM)**, under the Ministry of Electronics & Information Technology (MeitY), has organized the **SEMICON INDIA 2024** event on September 11th, 2024. The event was inaugurated by the Hon'ble Prime Minister of India, Shri Narendra Modi, who addressed the nation and highlighted on India's advancements in the semiconductor sector. On this occasion he quoted his vision that, All the electronic gadgets across the world will have India's Semiconductor chips in it. This event has showcased emerging opportunities in this field. UP Chief Minister, Yogi Adityanath global stakeholders, including CEOs, international delegates, and key Government officials chaired the event and highlighted on the need of semiconductor chips and its importance in making digital revolution in India and also extended their collaboration to engage with India in this semiconductor Mission. Department of E&CE arranged to attend this video conference, SEMICON INDIA 2024 through YouTube live session at 11:00 AM in JITD Auditorium, EEE Seminar Hall and CSE Seminar at JITD campus. **Dr. Ganesh D B**, Principal & Director, **Dr. Manjappa Sarathi**, Advisor, JITD, Heads & Deans of various departments, all the faculty members of JITD also attended the session. Dr. Ravikumar K I and Mr. Sharanabasaveshwara H B coordinated the event from Dept. of E&CE and Mr. Nagaraja Bodravara coordinated the event from Dept. of E&EE and Mr. Manjunath K G coordinated the event from Dept. of CS&E. 180 Students from CSE attended the session in CSE seminar hall. 240 Students from ISE and ECE attended the session in JITD Auditorium. 100 Students from EEE, ME and Civil students attended the session in EEE seminar hall.

JITD Tomorrow



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Affiliated to VTU Belagavi | Approved by AICTE, New Delhi | Recognized by UGC under 2(f) | Recognized by Govt. Of Karnataka

TRAINING AND PLACEMENT CELL

Placement Training

Phase IV (Technical Training) - 24 hours

 17/09/2024 to 20/09/2024
 9 am to 5 pm

In association with



Seventh Sense Talent Solutions

For Seventh Semester Engineering Students

Event Coordinators

Mr. Babusab
Dept. of CS&E

Mrs. Vinutha M
Dept. of IS&E

Dr. Prakash M Walavalkar
Dean (Training & Placement)

Dr. Ganesh D B
Principal & Director



Placement Training : Phase-IV (Technical Training) will be conducted for all the 7th semester students by the Training and Placement Cell, JITD in association with Seventh Sense Talent Solutions from 17/09/2024 to 20/09/2024.

Faculty - Outside World Interaction

Prerana Educational and Social Trust (R)
PES INSTITUTE OF TECHNOLOGY & MANAGEMENT
NH-206, SAGAR ROAD, SHIVAMOGGA-577204
Approved by AICTE, New Delhi | Affiliated to VTU, Belagavi | Recognized by Govt. of Karnataka
NBA Accredited Programs: CSE, CV, ECE, EEE & ME.

Dept. of Electronics & Communication Engineering
in associate with
IETE Student Chapter

SDP_{on}

“Circuit and System Simulation”

19th to 21st Sep-2024

Overview of SDP:
The three-day Student Development Program (SDP) focused on circuit and microcontroller based system design and simulation using freeware.

Objective of the SDP:
The students will develop professional skills that are required to design and simulate electronic circuits and microcontroller-based systems. With rapid validation and Interface skills, students can quickly build their projects using freeware.

Resource Persons:

Prof. Sharanabasaveshwara H. B. Jain Institute of Technology, Davanagere. Rushikesh Garkal JSPM University, Pune. Dr. Saba Fatima JSPM University, Pune.	Dr. Mahantesh P Mattada Associate Professor, Dept. of ECE, PESITM, Shivamogga. Prof. Srilakshmi Nayaka S. R. Assistant Professor, Dept. of ECE, PESITM, Shivamogga.
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Coordinators: **Dr. Mahantesh P. Mattada**
Associate Professor, Dept. of ECE, PESITM, Shivamogga.
Contact - 9762857508

Prof. Srilakshmi Nayaka S.R.
Assistant Professor, Dept. of ECE, PESITM, Shivamogga.
Contact - 9108295523

Dr. Om Prakash Yadav
Professor & Head
Dept. of ECE, PESITM, Shivamogga.

Dr. Yuvaraju B.N.
Principal
PESITM, Shivamogga.



Images of Faculty Outside World Interaction

Mr. Sharanabasaveshwara H B, Assistant Professor, Dept. of E&CE invited as Resource Person to conduct two sessions on Pspice Simulation in the SDP at Dept. of ECE, PESIT Shivmogga on 19/09/2024. He explained about Design and analysis of complex circuits in the Student Development Program.

Alumni Interaction



An Alumni interaction was organized by Electronics and Communication Engineering department. The event started with a warm welcome by the Co-ordinators, followed by brief introduction of the alumni, highlighting their career achievements since graduation.

Mr. Kaushik Itagi B, a Proud alumnus of Dept. of ECE, JIT has 8.4 years of diverse professional experience spanning software development, data science, and block chain technology. He has worked at Microsoft, where he contributed to building scalable software solutions. As a founding engineer at Sifchain, a crypto currency project, he played a pivotal role in developing block chain solutions and driving innovation in the DeFi space. Currently Kaushik Itagi B, working as a Senior Principal Engineer at Stefanini and will soon be joining Google. Kaushik Itagi B also pursuing a Postgraduate Diploma in Quantum Computing, exploring the future of technology. He inspired the students to focus on academics and encouraged to develop interpersonal skills necessary for interviews. He also motivated students not to lose hope and shared his hard times and how to

overcome the hurdles in the path. During the Interaction, Dr. Santosh Herur, Professor & Head, Dept. of ECE, Dr. Rajendra Soloni, Associate Professor, Dept. of ECE, and Event coordinators, Dr. Ravi Rayappa, Associate Professor and Mr. Sunil M, Assistant Professor, Dept. of ECE and the students of 3rd & 5th semester were present.

Aayudha Pooja Celebration



On the occasion of Dasara festival, Department of Electronics and Communication Engineering celebrated Aayudha Pooja. All the faculty members of E&CE Department were present during the celebration.

NAAC Internal Audit



NAAC Internal Audit was Conducted in the Department of E&CE. **Dr. Santosh Herur**, Professor & Head, Dept. of ECE ,IQAC-Director and **Dr. Rajendra Soloni**, NAAC Coordinator verified all the NAAC files and related documents and gave their suggestions in presenting and updating the files. **Dr. Ravikumar K I**, Accreditation Coordinator, Dept. of E&CE and all the Teaching and Non-teaching staff members of the Department were present during the Audit.

Forum ENCORE Inauguration and Fresher's Day


Arka Educational & Cultural Trust (Regd.)
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(A Unit of Jain Group of Institutions, Bangalore)
Department of Electronics and Communication Engineering


Cordially invites you to
Forum Inauguration & Fresher's Day
25 October 2024 at 10.00 A.M.
Venue: JITD Auditorium

Chief Guest:-
Mr. M S Jayachandra Aradhya
CEO & Founder
SILICON MICRO SYSTEMS, Bangalore

Forum Coordinator
Mr. Sharanabasaveshwara H B

Prof. & Head
Dr. Santosh Herur
ECE, JIT, Davangere

Advisor
Dr. Manjappa S.
JGI Group

Principal
Dr. Ganesh D B
JIT, Davangere

Program Flow

Invocation
Inauguration by Lighting the Lamp
Welcome Speech
Address by Forum Coordinator
Address by HoD
Introduction of Chief Guest
Address by Chief Guest
Address by Advisor
Felicitation to Toppers
Presidential Remarks
Vote of Thanks



Images of Forum ENCORE Inauguration and Fresher's Day

The Inauguration Function of the E&CE Department **Forum ENCORE** and Fresher's Day 'Celestia 2K24' was organized at JITD Auditorium. Chief Guest, Mr. M S Jayachandra Acharya, CEO &

Founder, Silicon Micro-Systems, Bangalore, JITD Advisor, Dr. Manjappa Sarathi, Principal & Director, Dr. Ganesh D B, Head of Department, Dr. Santosh Herur, Forum Coordinator, Prof. Sharanabasaveshwara H B and Student Forum representatives inaugurated the event by lightening the Lamp.

Forum Coordinator, **Prof. Sharanabasaveshwara H B** gave brief insights about the events conducted under Forum ENCORE and upcoming events planned to conduct for the benefit of students.

Dr. Santosh Herur, Professor and Head, Dept. of E&CE addressed the gathering about career opportunities in ECE and motivated students to actively participate in all the Forum activities and Placement trainings to enhance Personal and Professional skills. He Highlighted on the benefits of OBE and Accreditations & affiliations status of the Institution.

Chief guest, **Mr. M S Jayachandra Acharya** in his speech highlighted on the role of forums and clubs in colleges to update professional and personal skill of students, job opportunities in the core domain and its pre-requisites and also interacted with the students through an activity to explain about the qualities to become an entrepreneur in the field of engineering and technology.

Academic toppers for the Academic Year 2023-24 were felicitated with certificate and medals and the bright students were encouraged to take IEEE Student membership by sponsoring an amount of Rs. 500 from the Department Forum and Rs.750 from the Institute.

Dr. Ganesh D B, Principal and Director, JITD in his presidential remarks motivated students to make best use of Forum platform by actively participating in the events and suggested students to get benefits from the MSME and PMKK centres incubated in the institute to develop technical and professional skills. He also congratulated the academic toppers. All the students, Teaching and non-teaching staff members of E&CE were present during the function.

A Visit to Aikanthika Natural Farm



Images of Aikanthika Natural Farm

3rd semester students of E&CE department visited Aikanthika Natural Farm, in connection with Organic farming and waste management Module of Social Connect & Responsibility course. At the farm, Mr. Raghava, explained about organic and natural agricultural farming methods and how chemical farming harms ecological balance overall, water supplies, and soil health. He also underlined the need of implementing sustainable alternatives to guarantee a more robust and healthy agricultural system. Course coordinator, Prof. Deepthi G B along with Prof. Savita Mamadapur, Prof. Sunil M and Instructor, Mr. Arunkumar B N Coordinated the visit.

Student Achievement : Campus Placement

JGI Jain Institute of Technology, Davangere
(Affiliated to VTU, Belagavi)

JIT Placement
Congratulations

Batch : 2024-25
Branch : E&CE

For Getting Selected in **Varun Kumar B V** **Disha S Asundi**

atandra KRYKARD
Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD, Placement Coordinators, Teaching & Non-Teaching Staffs and Students.

CTC : 4.1 LPA

9663868258 www.jitd.ac.in placement@jitd.in

Final year students of E&CE Department, **Mr. Varun Kumar B V** and **Ms. Disha S Asundi**, has been placed in **Atandra Company** with a salary package of **4.1 LPA** during the recent placement drive. Hearty congratulations and best wishes to both the students from College Management, Principal, Advisor, Dean-Training & Placement Cell, HOD, Placement Coordinators, Teaching & Non-teaching Staffs and students.

Placement Training --- 11/11/2024

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TRAINING & PLACEMENT CELL
Placement Training

Phase V (Training & Placement) - 24 hours

11/11/2024 to 15/11/2024
9 am to 5 pm

In association with **Seventh Sense Talent Solutions**

For Seventh Semester Engineering Students

Event Coordinators
Dr. Niranjana M
Dept. of Physics
Mr. Sunil M
Dept. of ECE

Dr. Prakash M Walavalkar
Dean (Training & Placement)

Dr. Ganesh D B
Principal & Director



The Training and placement cell organized placement training for the 7th semester engineering students in association with Seventh sense talent solutions Bengaluru. On Day-1. The trainers namely, Mr. Muheeb, Ms. Shriya, Mr. Surya, Ms. Nethra, Conducted 3 sessions on Number Systems, Collaboration & Team Work, Idioms and Phrases, Clocks, for 7th Semester BE students. Ms. Ashwini D, constantly interacted with trainers and took feedbacks. During Training Snacks and Food is distributed to all the 7th Semester Students and Placement Coordinators. Training Event Coordinators, Dr. Niranjana M, Mr. Sunil M, Coordinated with the Seventh Sense Talent Solutions Trainers.

Faculty Achievement : Ph.D Convocation



Dr. Ravikumar K. I., Assistant Professor, Dept. of E&CE, received his Ph.D. Convocation from Jain University in an esteemed event organized at the Prestige Srihari Khoday Centre for Performing Arts, Konanakunte, Bengaluru. He successfully completed Ph.D. defence viva on the research topic entitled **“Design and Implementation of RRAM for ANN Based Applications”** on 30th April 2024. Hearty Congratulations and best wishes on his accomplishment from College Management, Advisor, Principal, HOD, Teaching & Non-teaching staff members.

Student's Achievement



Mr. Adarsh P and **Ms. Chinmayi S** of 3rd Sem, A section, Dept. of E&CE participated in the final round of IDEATHON 2024 at CIT college, Gubbi, Tumkur district on 21/11/2024, under the guidance of **Mrs. Savitra C T**, Assistant Professor, Department of E&CE. The Team is encouraged by the CIT College by providing them participating certificates. Heartiest congratulations and best wishes to the team from College Management, Advisor, Principal, HOD and Teaching & Non-Teaching staff members.

Faculty's Participation



Dr. Ravikumar K.I, Assistant Professor, Dept. of E&CE has participated in the **SAMAVESHA-7** organized by the Indian Science Technology and Engineering Facilities Map in collaboration with G M University, Davangere on 26th November 2024. He was felicitated with certificate and memento for representing our Institute.

Course Activity: Heritage Walk



Images of Heritage Walk

3rd semester A and B section students of E&CE department conducted a 'Heritage walk' to Hampi and Hospet for activity-based learning about historical places and methods of water conservation as a part of Social Connect and Responsibility course. The students visited historical temples and places in Hampi, Hospet and Anjanadri hills. Prof. Savitha Mamadapur and Prof. Sharanabasaveshwara H B accompanied the students and course coordinator, Prof. Deepti G B coordinated the visit.

Kannada Rajyotsava and Ethnic Day Celebration

Jain Institute of Technology, Davangere celebrated Kannada Rajyotsava and Ethnic Day on 30th November 2024. Chief Guest, **Mr. Mahalingappa A**, Rajyotsava-2024 awardee inaugurated the event by hoisting karnataka Flag. He addressed the gathering about the great heritage and culture of karnataka and importance of celebrating kannada rajyotsava. **Mr. Santosh A D**, Mimicry and film artist was invited as the Judge for the event. **Dr. Manjappa Sarathi**, Advisor, JITD, **Dr. Ganesh D B**, Principal & Director, JITD, **Dr. Santosh M Nejakar**, Convener of the event and **Prof. Murulidhar S**, Cultural Coordinator, Heads of various departments, Teaching and Non teaching staff members were present during the event. All the students of various departments presented themes and models representing rich culture, heritage, historical places and great kings and dynasties of Karnataka through procession and performances.

Theme: Karavali Kalarava

Department of Electronics & Communication celebrated Ethnic Day and Kannada Rajyotsava with the theme **Karavali Kalarava**. They exhibit the rich heritage of karavali highlighting Yakshagana, hulivesha through the dance performances. All students and faculty members actively participated in ethnic day and dressed up in traditional ethnic wear.





Student's Participation



5th Semester students of E&CE Department, **Ayush G Y** and **Nandan Prakash P** participated in the Ideathon Competition held during IMPULSE event conducted by the Quartz club of ECE Department on 29th November 2024. Hearty Congratulations and best wishes to both the students from College Management, Advisor, Principal, HOD and Teaching and Non-teaching Staff members.

ಜೈನ್ ಕಾಲೇಜು: ಕನ್ನಡ ಉತ್ಸವ, ಎಡ್ಲಿಕ್ ಡೇ ಸಂಭ್ರಮಾಚರಣೆ



ದಾವಣಗೆರೆ: ನಗರದ ಜೈನ್ ತಾಂತ್ರಿಕ ಮಹಾವಿದ್ಯಾಲಯದಲ್ಲಿ ಕನ್ನಡ ರಾಜ್ಯೋತ್ಸವ ಹಾಗೂ ಎಡ್ಲಿಕ್ ಡೇ ಆಚರಣೆಯ ಅಂಗವಾಗಿ ಕನ್ನಡ ನಾಡಿನ ಸಂಸ್ಕೃತಿ ಮತ್ತು ಹಿರಿಮೆಯನ್ನು ಪ್ರತಿಬಿಂಬಿಸುವ ಸಾಂಸ್ಕೃತಿಕ ಕಾರ್ಯಕ್ರಮವನ್ನು ಧ್ವಜರೋಹಣ ನೇರವೆಸುವ ಮೂಲಕ ಚಾಲನೆ ನೀಡಲಾಯಿತು. ವಿದ್ಯಾರ್ಥಿಗಳು ನಾಡಿನ ಸಂಸ್ಕೃತಿಯನ್ನು ಪ್ರತಿಬಿಂಬಿಸುವ ಹಬ್ಬಗಳು, ಹೆಸರಾಂತ ಸಾಮ್ರಾಜ್ಯದ ರಾಜರುಗಳು, ಕವಿಗಳು ಹಾಗೂ ಕಲಾವಿದರನ್ನು ವೇಷ ಧರಿಸಿ ಪ್ರದರ್ಶನ ಹಾಗೂ ಮೆರವಣಿಗೆ ಮಾಡಿದರು. ರಾಜ್ಯೋತ್ಸವ ಪ್ರಶಸ್ತಿ ಪುರಸ್ಕೃತ, ಹಿರಿಯ ಕಲಾವಿದ ಎ.ಮಹಲಿಂಗಪ್ಪ, ಮಿಮಿಕ್ರಿ ಕಲಾವಿದ ಎಂ.ಡಿ.ಸಂತೋಷ್, ಕಾಲೇಜಿನ ಪ್ರಾಚಾರ್ಯ ಡಾ.ಡಿ.ಬಿ.

ಗಣೇಶ್, ಕಾರ್ಯಕ್ರಮದ ಸಂಚಾಲಕರಾದ ಡಾ.ಸಂತೋಷ್ ಎ.ನೇಜಕರ್, ಸಲಹೆಗಾರರಾದ ಡಾ. ಮಂಜಪ್ಪ ಸಾರಥಿ ಹಾಗೂ ಸಾಂಸ್ಕೃತಿಕ ಸಂಯೋಜಕ ಎಸ್. ಮುರಳಿದರ್ ಮತ್ತು ಎಲ್ಲಾ ವಿಭಾಗದ ಮುಖ್ಯಸ್ಥರು, ಪ್ರಾಧ್ಯಾಪಕರು ಉಪಸ್ಥಿತರಿದ್ದರು.



ದಾವಣಗೆರೆ: ನಗರದ ಜೈನ್ ತಾಂತ್ರಿಕ ಮಹಾವಿದ್ಯಾಲಯದಲ್ಲಿ ಕನ್ನಡ ರಾಜ್ಯೋತ್ಸವ ಹಾಗೂ ಎಡ್ಲಿಕ್ ಡೇ ನಿಮಿತ್ತ ಆಯೋಜಿಸಿದ್ದ ಸಾಂಸ್ಕೃತಿಕ ಕಾರ್ಯಕ್ರಮದಲ್ಲಿ ವಿದ್ಯಾರ್ಥಿಗಳು ನಾಡಿನ ಸಾಂಸ್ಕೃತಿಕ ವೈಭವ ಬಿಂಬಿಸುವ ವೇಷಭೂಷಣ ಧರಿಸಿ ಗಮನ ಸೆಳೆದರು.

ಎಡ್ಲಿಕ್ ಡೇ ಸಂಭ್ರಮ



ದಾವಣಗೆರೆ: ನಗರದ ಜೈನ್ ತಾಂತ್ರಿಕ ಮಹಾವಿದ್ಯಾಲಯದಲ್ಲಿ ಶನಿವಾರ ವಿಜೃಂಭಣೆಯಿಂದ ಕನ್ನಡ ರಾಜ್ಯೋತ್ಸವ ಹಾಗೂ ಎಡ್ಲಿಕ್ ಡೇ ಆಚರಿಸಲಾಯಿತು.

ಕಾರ್ಯಕ್ರಮದಲ್ಲಿ ವಿದ್ಯಾರ್ಥಿಗಳೆಲ್ಲರೂ ಕನ್ನಡ ನಾಡಿನ ಸಂಸ್ಕೃತಿ ಪ್ರತಿಬಿಂಬಿಸುವ ಹಬ್ಬಗಳು, ಹೆಸರಾಂತ ರಾಜರು, ಕವಿಗಳು ಹಾಗೂ ಕಲಾವಿದರ ಹಾಡುಗಳಿಗೆ ನೃತ್ಯ ಮಾಡಿದರು. ಅಲ್ಲದೇ, ದೇವೀ ಉಡುಗೆ ಉಟ್ಟು ಮೆರವಣಿಗೆಯಲ್ಲಿ ಮುಂಚಿದರು.

ಹಿರಿಯ ಚಿತ್ರಕಲಾವಿದ ಎ.ಮಹಲಿಂಗಪ್ಪ ಮಾತನಾಡಿ, ಕನ್ನಡ ನಾಡಿನ ಹಿರಿಮೆ ಬಗ್ಗೆ ಸಂಕ್ಷಿಪ್ತವಾಗಿ ತಿಳಿಸಿದರು.

ನಾನಾ ಸ್ಪರ್ಧೆಗಳ ತೀರ್ಮಾನಗಾರರಾಗಿ ಮಿಮಿಕ್ರಿ ಕಲಾವಿದ ಎ.ಡಿ. ಸಂತೋಷ್ ಕಾರ್ಯನಿರ್ವಹಿಸಿದರು. ಕಾಲೇಜಿನ ಪ್ರಾಚಾರ್ಯ ಡಾ.ಡಿ.ಬಿ. ಗಣೇಶ್, ಡಾ.ಸಂತೋಷ್ ಎಂ. ನೇಜಕರ್, ಡಾ.ಮಂಜಪ್ಪ ಸಾರಥಿ, ಎಸ್. ಮುರಳಿದರ್ ಮತ್ತಿತರರು ಭಾಗವಹಿಸಿದ್ದರು.

ಜೈನ್ ಕಾಲೇಜಿನಲ್ಲಿ ವಿಜೃಂಭಣೆಯ ಕನ್ನಡ ಉತ್ಸವ ಹಾಗೂ ಎಡ್ಲಿಕ್ ಡೇ ಸಂಭ್ರಮಾಚರಣೆ

ದಾವಣಗೆರೆ: ನಗರದ ಜೈನ್ ತಾಂತ್ರಿಕ ಮಹಾವಿದ್ಯಾಲಯದಲ್ಲಿ ಕನ್ನಡ ರಾಜ್ಯೋತ್ಸವ ಹಾಗೂ ಎಡ್ಲಿಕ್ ಡೇ ಆಚರಣೆಯ ಅಂಗವಾಗಿ ಕನ್ನಡ ನಾಡಿನ ಸಂಸ್ಕೃತಿ ಮತ್ತು ಹಿರಿಮೆಯನ್ನು ಪ್ರತಿಬಿಂಬಿಸುವ ಸಾಂಸ್ಕೃತಿಕ ಕಾರ್ಯಕ್ರಮವನ್ನು ಧ್ವಜರೋಹಣ ನೇರವೆಸುವ ಮೂಲಕ ಚಾಲನೆ ನೀಡಲಾಯಿತು. ವಿದ್ಯಾರ್ಥಿಗಳು ನಾಡಿನ ಸಂಸ್ಕೃತಿಯನ್ನು ಪ್ರತಿಬಿಂಬಿಸುವ ಹಬ್ಬಗಳು, ಹೆಸರಾಂತ ಸಾಮ್ರಾಜ್ಯದ ರಾಜರುಗಳು, ಕವಿಗಳು ಹಾಗೂ ಕಲಾವಿದರನ್ನು ವೇಷ ಧರಿಸಿ ಪ್ರದರ್ಶನ ಹಾಗೂ ಮೆರವಣಿಗೆ ಮಾಡಿದರು. ಮಿಮಿಕ್ರಿ ಅಭಿಗಣಿ ಆಗಮಿಸಿದ್ದ 2024ರ ರಾಜ್ಯೋತ್ಸವ ಪ್ರಶಸ್ತಿ ಪುರಸ್ಕೃತ ಟ್ರಿ ಮಹಲಿಂಗಪ್ಪ ಎ.ಡಿ.ಬಿ. ಹಿರಿಯ ಚಿತ್ರಕಲಾವಿದ ಎ.ಮಹಲಿಂಗಪ್ಪ ಮಾತನಾಡಿ, ಕನ್ನಡ ನಾಡಿನ ಹಿರಿಮೆ ಬಗ್ಗೆ ಸಂಕ್ಷಿಪ್ತವಾಗಿ ತಿಳಿಸಿದರು. ತೀರ್ಮಾನಗಾರರಾಗಿ ಆಗಮಿಸಿದ್ದ ಚಲನಚಿತ್ರ ಮತ್ತು ಮಿಮಿಕ್ರಿ ಕಲಾವಿದರಾದ ಟ್ರಿಯುಪ್ ಸಂತೋಷ್ ಎ.ಡಿ. ಡಾ. ಮಂಜಪ್ಪ ಸಾರಥಿ ಹಾಗೂ ಸಾಂಸ್ಕೃತಿಕ ಸಂಯೋಜಕರಾದ ಟ್ರಿ ಮುರಳಿದರ್ ಎಸ್ ಮತ್ತು ಪ್ರಾಧ್ಯಾಪಕರು ಉಪಸ್ಥಿತರಿದ್ದರು.



ಜೈನ್ ಕಾಲೇಜಿನಲ್ಲಿ ರಾಜ್ಯೋತ್ಸವ



ದಾವಣಗೆರೆಯ ಜೈನ್ ತಾಂತ್ರಿಕ ಮಹಾವಿದ್ಯಾಲಯದಲ್ಲಿ ಶನಿವಾರ ಕರ್ನಾಟಕ ರಾಜ್ಯೋತ್ಸವದ ಅಂಗವಾಗಿ ನಡೆದ ಮೆರವಣಿಗೆಯಲ್ಲಿ ವಿದ್ಯಾರ್ಥಿಗಳು ಯಶ್ವಗಾನದ ವೇಷಭೂಷಣ ಧರಿಸಿ ಗಮನ ಸೆಳೆದರು.

ಪ್ರಜಾವಾಣಿ ವಾರ್ತೆ

ದಾವಣಗೆರೆ: ನಗರದ ಪ್ರತಿಷ್ಠಿತ ಜೈನ್ ತಾಂತ್ರಿಕ ಮಹಾವಿದ್ಯಾಲಯದಲ್ಲಿ ಶನಿವಾರ ಕರ್ನಾಟಕ ರಾಜ್ಯೋತ್ಸವ ಹಾಗೂ ಎಡ್ಲಿಕ್ ಡೇ ಕಾರ್ಯಕ್ರಮ ನಡೆಯಿತು.

ವಿದ್ಯಾರ್ಥಿಗಳು ನಾಡಿನ ಹೆಸರಾಂತ ರಾಜರು, ಕವಿಗಳು ಹಾಗೂ ಕಲಾವಿದರ ವೇಷಭೂಷಣ ಧರಿಸಿ ನೃತ್ಯ ಮಾಡಿ, ಹಾಡು ಹಾಡುವ ಮೂಲಕ ಮೆರವಣಿಗೆಯಲ್ಲಿ ಪಾಲ್ಗೊಂಡರು.

ರಾಜ್ಯೋತ್ಸವ ಪ್ರಶಸ್ತಿ ಪುರಸ್ಕೃತ, ಚಿತ್ರ ಕಲಾವಿದ ಮಹಲಿಂಗಪ್ಪ ಎ. ಮುಖ್ಯ ಅತಿಥಿಯಾಗಿ ಭಾಗವಹಿಸಿದ್ದರು. ಮಿಮಿಕ್ರಿ ಕಲಾವಿದ ಸಂತೋಷ್ ಎ.ಡಿ. ತೀರ್ಪುಗಾರರಾಗಿ ಭಾಗವಹಿಸಿದ್ದರು.

ಕಾಲೇಜಿನ ಪ್ರಾಚಾರ್ಯ ಗಣೇಶ್ ಡಿ.ಬಿ., ಕಾರ್ಯಕ್ರಮದ ಸಂಚಾಲಕ ಸಂತೋಷ್ ಎಂ.ನೇಜಕರ್, ಸಲಹೆಗಾರ ಮಂಜಪ್ಪ ಸಾರಥಿ ಹಾಗೂ ಸಾಂಸ್ಕೃತಿಕ ಸಂಯೋಜಕ ಮುರಳಿದರ್ ಎಸ್ ಉಪಸ್ಥಿತರಿದ್ದರು.

IIC : A Session on “Problem Solving & Ideation Workshop”

NBA **INSTITUTION'S INNOVATION COUNCIL** **ACCREDITED**

Arka Educational and Cultural Trust Regd.

Jain Institute of Technology, Davangere
(A Unit of Jain Group of Institutions, Bengaluru.)

JITD Institutions Innovation Council
In Association With
Department of Electronics and Communication Engineering
Organizes
A session on "Problem Solving and Ideation Workshop"

On: 6-12-24
11 am
JITD Auditorium

Resource Person
Mr. Manjunatha M
Data Analyst
Abeyaantrix Edu Soft.

Mrs. Anusha T S
IIC, Coordinator

Mr. Harish K S
Convener, JITD, IIC

Dr. Mouneshachari S
Vice President, JITD, IIC

Dr. Santosh Herur
Prof. & Head

Dr. Manjappa Sarathi
Advisor, JITD

Dr. Ganesh D.B
Principal and Director



JITD Institution's Innovation Council (IIC) in association with the Department of Electronics and Communication Engineering organized A session on “**Problem solving and Ideation workshop**” on 06th December, 2024 at JITD Auditorium. **Mr. Manjunath M, Data analyst, Abeyaantrix Edu Soft.** and a Proud Alumni of E&CE dept, JITD was invited as the Resource person for the Program. **Dr. Ganesh D B**, Principal & Director, and **Dr. Manjappa Sarathi**, Advisor, JITD conveyed their best wishes in their absence. **Dr. Santosh Herur**, Professor and Head, Department of E&CE addressed the gathering and encouraged the students to upskill their knowledge in Problem solving and Ideation. **Prof. Harisk K S**, Convener and **Prof. Anusha T S**, IIC Coordinator, Dept. of E&CE were also present during the program. A total of 50 students from 3rd semester of various department

attended the Workshop. All the teaching and non-teaching staff members of the Institute were also present during the event.

Course Activity : Food Walk

ARKA Educational cultural trust (R)

JGI JAIN INSTITUTE OF TECHNOLOGY, Davanagere
(A unit of Jain Group of Institutions, Bengaluru)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
(Accredited by NBA, New Delhi)

FOOD walk

In The View of Social Connect And Responsibility course

Date: 9/12/2024
Venue: E&C Department Block
Time: 2:00 pm
Prof. Deepthi GB
Course Coordinator

Dr. Santosh Herur
Head of Department

Dr. Ganesh DB
Principal & Director



Food Walk activity was conducted for the 3rd semester A section students on 9th December 2024 in the department of E&CE as a part of Social Connect and Responsibility Course : Module 5. Students presented various dishes. Mr. Manjunath and Mrs. Geetha M, Yoga teachers, were invited as judges for the event. Course Coordinator Prof. Deepthi G B and all the faculties of E&CE Dept. were present. The judges evaluated the activity based on culinary practices, taste, hygiene and presentation skills.

Chinmayi S & Aishwarya team ranked first and Akash , Adarsh & Dhruvaraj team ranked second among the participants.

Skill Development Program on Embedded system for Project/Product Development



Six days **Skill Development Program on Embedded system for Project/Product Development** was organized by the department of E&CE in Association with Abeyaantrix Softlab, Davangere from 05th December 2024 to 12th December 2024. During the Program students were taught about the

working of Bluetooth and its programming by using the Bluetooth module, Wi-Fi module and its Programming. Abeyaantrix team supported and trained students in building their Mini-project. On 13th December 2024 a Valedictory function of the Skill Development Program on Embedded system for Project/Product Development was organized in EEE Seminar Hall, JITD. **Mr. Shivraj V S**, Manager, Abeyaantrix Edusoft LLP and **Mr. Rakesh K B**, Embedded Engineer, Abeyaantrix Edusoft LLP were invited as the chief guest for the event. Principal & Director, **Dr. Ganesh D B** addressed and congratulated students on completing their training to complete mini project. **Dr. Santosh Herur**, HOD, dept. of ECE addressed students and highlighted on the importance of entrepreneurship and choosing a good Final year project. Students shared their feedback on the SDP and all the students were given participation certificates. Prof. Sharanabasaveshwara H B and Prof. Deepthi G B Coordinated the event. All the faculty members and 5th semester students of ECE Dept. were present during the event.

TAP Academy Internship Drive



The Training & Placement Cell of Jain Institute of Technology, Davanagere, organized a successful internship drive in collaboration with TAP Academy on December 12, 2024. The drive, open to

students from all branches, was conducted by the HR team lead by Mr. Mallikarjun from TAP Academy, with active participation from students across various disciplines. The event was coordinated by Dr. Prakash M Walavalkar (Dean, Training & Placement), and placement coordinators from different departments. The drive concluded with 13 students being selected, including 12 from the Computer Science & Engineering (CSE) branch and one from the Electronics & Communication Engineering (ECE) branch. TAP Academy, known for its focus on technical training in areas such as Java, Python, and Data Science, provided a valuable platform for students to demonstrate their skills and secure internship opportunities. At the conclusion of the event, Dr. Ganesh D B, Principal & Director, congratulated the selected students, encouraging them to make the most of this opportunity to enhance their professional and technical expertise. The Training & Placement Cell extends its gratitude to TAP Academy, the HR team, faculty members, placement coordinators, and students for their enthusiastic participation and efforts in making the event a success.

Project Exhibition : P-EXPO-2024



Images of P-EXPO

P-EXPO-2024, A Project and Product Exhibition was organized at Jain Institute of Technology. All the final year students of E&CE Department actively participated and demonstrated their projects and presented their innovative ideas with the help of charts and models. A Total of 18 teams participated in the event.

Dr. Mahantesh P Mattada, Associate Professor, Dept. of ECE, PESITM, Shimogga has been invited as the external examiner for the evaluation of projects. He appreciated the efforts of students and encouraged them. **Dr. Ganesh D B**, Principal & Director, JITD and **Dr. Santosh Herur**, Professor & Head, Dept. of E&CE were also assessed student's projects.

Among all the projects exhibited, the following **Two Projects** were ranked as first and second by the Evaluator.

FIRST PLACE

The Projectwork entitled **"EARLY DETECTION OF GLAUCOMA USING DEEP LEARNING TECHNIQUE"** presented by the students : **Kiran Kumar S H, Varun Kumar B V, Virakthi Matam Rakshith and Kushal A N** under the guidance of **Prof. Pushpalatha O**, Assistant Professor, Dept. of E&CE has got First place in the Project Exhibition.



SECOND PLACE

The Project work entitled **"ENHANCING BABY SAFETY IN CAR USING IOT"** presented by the students : **Bhumika S, Hemavathi G, Meghana N S and Pruthvi B U** under the guidance of **Dr. Rajendra Soloni**, Associate Professor, Dept. of E&CE has got Second place in the Project Exhibition.



Course Activity : Food Walk



Images of Food Walk activity

Food Walk activity was conducted for the 3rd semester B section students on 21st December 2024 in the department of E&CE as a part of Social Connect and Responsibility Course : Module 5. Students presented various dishes. Mrs. Renuka B C, Librarian, JITD invited as judge and Course Coordinator Prof. Deepthi G B coordinated the event. The evaluation of the activity was based on culinary practices, taste, hygiene and presentation skills. Among all the participants 2 teams ranked first and 2 teams second. Thrupthi & Shaziya Siddiqua teams got first place and Priyanka P H & Rajesh Shivananchi teams got second place.

Address by Dr. G R Udupi----01/01/2025



Dr. G R Udupi, Director R&D, Angadi Institute of Technology, Belagavi addressed all the faculty members of E&CE and EEE department regarding R&D proposal writing and idea submission. He shared insights about proposal writing ,patent, grants and its procedure and innovation & design thinking applications in real life.

Student's Participation



5th semester of ECE Department **Manoj S, Manoj K J, Abhishek K H and Prajwal N Y** has presented a paper titled **"Human following robot using Arduino UNO"** under the guidance of **Prof. Savitra Totada** in a National Conference on "Emerging Trends in science, engineering & technology", organized by City Engineering College, Bangalore on 27th & 28th of December in online mode.

A Technical Talk on “IoT and Its Applications--11/01/2025



Arka Educational & Cultural Trust (R)
Jain Institute of Technology, Davangere
(A Unit of Jain Group of Institutions, Bangalore)
Department of Electronics and Communication Engineering



Organizing
Technical Talk
on
IoT and Its Applications

Resource Person
Mr. Mantesh Shirur
Embedded Software Engineer
Abeeyantrix Softlab, Davangere

Coordinator Dr. Rajendra Soloni Associate Professor, ECE	Convener Dr. Santosh Herur Professor & Head, ECE	Principal Dr. Ganesh D B JIT, Davangere
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Targeted Audience : 1st Sem, ECE Students
Venue: Auditorium

Date: 11-01-2025 **Time: 9.30 AM to 11.00 AM**



A Technical talk on “IoT and Its Applications” was organized by the Department of Electronics and Communication Engineering for 1st semester E&CE students in CSE Seminar Hall. Mr. Mantesh Shirur, Embedded Software Engineer, Abeeyantrix Edusoft, Davangere was invited as a resource person for this technical talk. He briefly explained about IoT, its architecture and applications & importance of IoT in different domains like agriculture, healthcare, vehicular. He has also discussed about IoT analytics. This technical event was organized as a course activity related to the course Introduction to IoT and it was organized by the course coordinators Dr. Rajendra Soloni, Associate Professor, and Dr. Santosh Herur, Professor and Head, Dept. of E&CE. At the beginning of this technical talk Ms. Shruthi S M, HR Manager, Abeeyantrix Edusoft, Davangere has briefed about the company.

Course Activity: 'Think-Pair-Share'



Images of course activity

Dr. Santosh Herur, Professor & Head, Department of E&CE and Course coordinator of Introduction to IoT conducted a seminar on "IoT Case Studies" for first year students. He congratulated to all J-section E&CE students for successful completion of Think-Pair-Share Activity (Group Activity) i.e., Seminar on IoT Case Studies and conveyed his wishes for their examinations.

Course Activity: 'Think-Pair-Share'



Images of course activity

Dr. Santosh Herur, Professor & Head, Department of E&CE and Course coordinator of Introduction to IoT conducted a seminar on **"IoT Case Studies"** for first year students. He congratulated to all H-section E&CE students for successful completion of Think-Pair-Share Activity (Group Activity) i.e., Seminar on IoT Case Studies and conveyed his wishes for their examinations.

Awareness Session on Placement Training



Images of awareness session on placement training

The Training and Placement Cell of Jain Institute of Technology conducted an awareness session for sixth-semester ECE students, stressing the importance of attendance and participation in the placement training starting March 3, 2025. Students were encouraged to earn certifications through Infosys Springboard, LinkedIn Learning, and NPTEL to boost career prospects. They were also introduced to VTU's Xcelerator platform for job opportunities and career resources. The session, led by **Dr. Prakash M. Walavalkar**, **Dr. Ravi Rayappa**, and **Mr. Sunil M.**, focused on skill development and career readiness.

Project Proposals Approved for 48th series KSCST Sponsorship

In the 48th series of Student Project Programme (SPP): 2024-25, **Six Proposals** from the department of E&CE has been selected and sponsored. Hearty Congratulations to both the Project Guides and Students from College Management, Advisor, Principal, HOD and all the faculty members.

Project Team-1

The project proposal titled **“PARALYSIS PATIENT HEALTH MONITORING USING FLEX SENSORS”** was submitted by the following students under the guidance of **Dr. RAVI RAYAPPA.**, Associate Professor, Dept. of E&CE. An amount of **Rs. 4,500.00/-** has been sanctioned.



Project Team Members



ARFATH PASHA A



YAQUB KHAN



SAMREEN BHANU



**KASHIFA HALEEMA
DK**

Project Team-2

The project proposal titled **“REAL TIME INTRAVENOUS(IV) DRIP MONITORING SYSTEM FOR SMART HEALTHCARE FACILITIES USING LOAD CELL”** was submitted by the following students under the guidance of **Dr. RAVIKUMAR K.I.**, Assistant Professor, Dept. of E&CE. An amount of **Rs. 4,500.00/-** has been sanctioned.



Project Team Members



SUPRITHA C L



**SUVARNA S
KOPPAD**



CHETHAN R C



SHASHANK T

Project Team-3

The project proposal titled “**EARLY DETECTION OF GLAUCOMA USING DEEP LEARNING TECHNIQUES**” was submitted by the following students under the guidance of **Prof. PUSHPALATHA O.**, Assistant Professor, Dept. of E&CE. An amount of **Rs. 3,500.00/-** has been sanctioned.



Project Team Members



KIRAN KUMAR S H



**VIRAKTHI MATAM
RAKSHITH**



VARUN KUMAR B V



KUSHAAL A N

Project Team-4

The project proposal titled “**DESIGN AND IMPLEMENTATION OF AUTOMATED BLOOD BANK USING EMBEDDED SYSTEMS**” was submitted by the following students under the guidance of **Prof. SUNIL.M.**, Assistant Professor, Dept. of E&CE. An amount of **Rs. 4,500.00/-** has been sanctioned.



Project Team Members



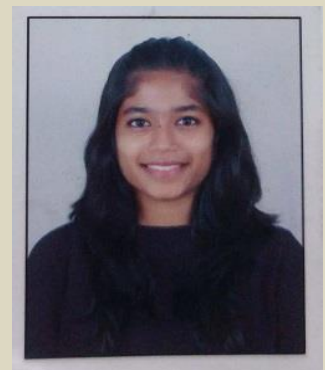
DISHA S ASUNDI



ANANYA V



SANJANA K M



SHEETAL M S

Project Team-5

The project proposal titled **“LPG GAS LEAKAGE DETECTION AND AUTOMATIC SHUTDOWN SYSTEM WITH REAL TIME NOTIFICATION”** was submitted by the following students under the guidance of **Prof. ANUSHA T S.**, Assistant Professor, Dept. of E&CE. An amount of **Rs. 4,500.00/-** has been sanctioned.



Project Team Members



DARSHAN S



AMARANAIK L



SINDHUSHREE M J



HARSHITHA V M

Project Team-6

The project proposal titled **“RAILWAY TRACK CRACK AND OBJECT DETECTOR USING IOT”** was submitted by the following students under the guidance of **Prof. NAYANA.T.**, Assistant Professor, Dept. of E&CE. An amount of **Rs. 4,000.00/-** has been sanctioned.



Project Team Members



DARSHAN M



MALTESHA B M



ADARSH S S



VINAYAK C M

Technical talk on “Advancement in AI and Career Opportunities

JGI **N3T** **NAAC GRADE A+** **IQAC**

Arka Educational & Cultural Trust (R)
Jain Institute of Technology, Davangere
(A Unit of Jain Group of Institutions, Bangalore)
Department of Electronics and Communication Engineering

Organizing
Technical Talk
on
Advancement in AI and its
Career Opportunities

Resource Person
Mr. Mallesh V S
Technical Team Manager
Abeyaantrix Edusoft, Davangere

Coordinator
Mr. Sharanabasaveshwara H B
Assistant Professor, ECE

Convener
Dr. Santosh Herur
Professor & Head, ECE

Principal
Dr. Ganesh D B
JIT, Davangere

Targeted Audience : 6th Sem, ECE Students
Venue: FF123

Date: 17-03-2025 **Time: 11.30 AM to 01.00 PM**



Images of technical talk

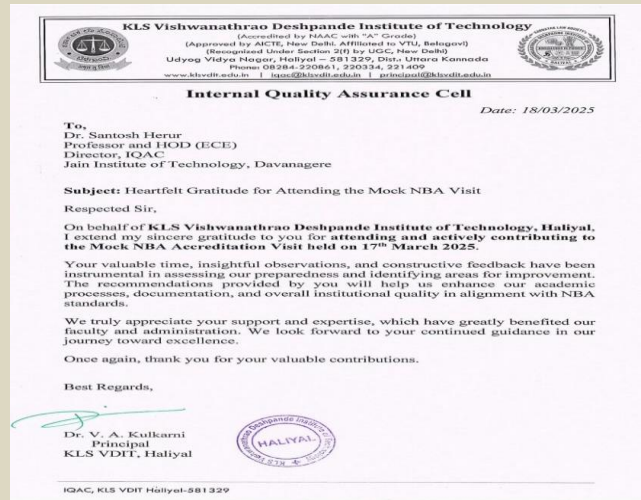
The Department of Electronics and Communication Engineering organized a technical talk on "Advancement in AI and Career Opportunities" for 6th-semester E&CE students. **Mr. Mallesh V S**, Technical Team Manager at Abeyaantrix Edusoft, Davangere, provided insights into AI advancements like **machine learning, deep learning, NLP, and computer vision** and their impact on industries such as healthcare, finance, and automation. He discussed ethical challenges, career paths in AI (AI engineering, data science, robotics), and essential skills like **Python, TensorFlow, and data analysis**. Students were encouraged to engage in AI projects, certifications, and research to enhance their employability.

Student's Participation : Paper Presentation



6th semester students of ECE department participated in the **PAPYRUS**, a state level technical paper presentation event held at BIET, Davanagere on 17th March 2025. A total 7 students participated and presented their technical papers during the event.

Faculty Outside World Interaction -- 17/03/2025

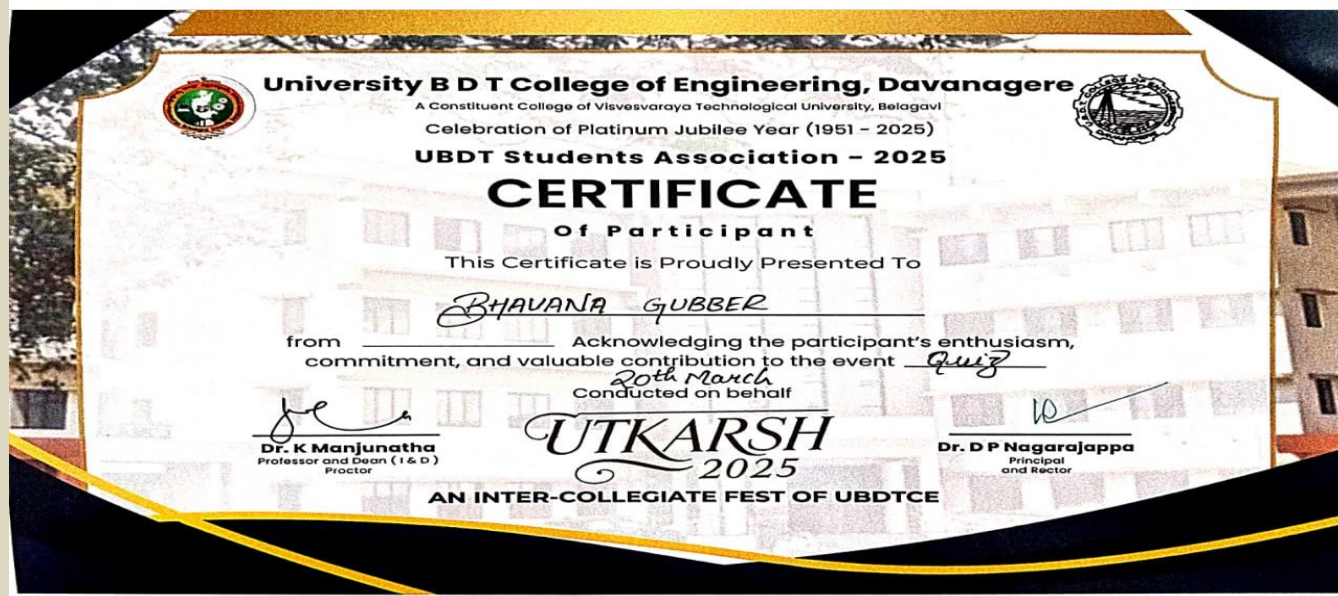
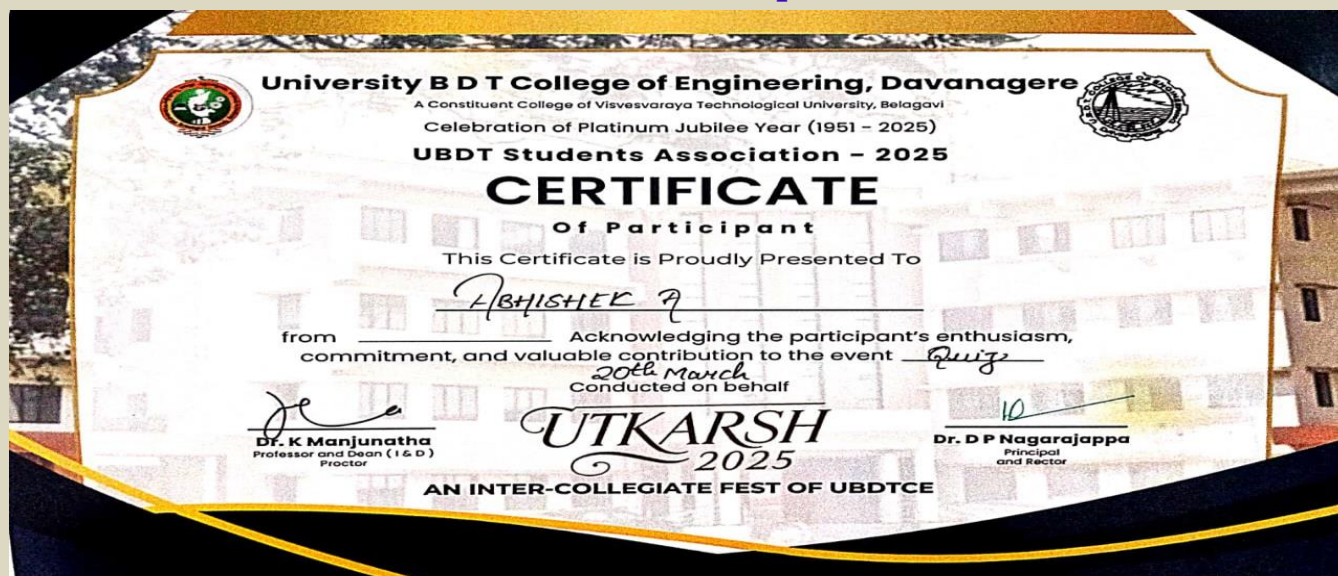


Images of faculty outside world interaction

Dr. Santosh Herur, Professor and Head, Dept. of E&CE and Director, IQAC of Jain Institute of Technology, Davangere visited KLS Vishwanathrao Deshpande Institute of Technology (KLS Vdit), Haliyal as an **Audit Expert member** for the **Mock NBA Accreditation** Visit scheduled on **17th March 2025**. His expertise and valuable insights played a crucial role in assessing their preparedness to face NBA Accreditation which they are on the verge of NBA pre-qualifiers submission.

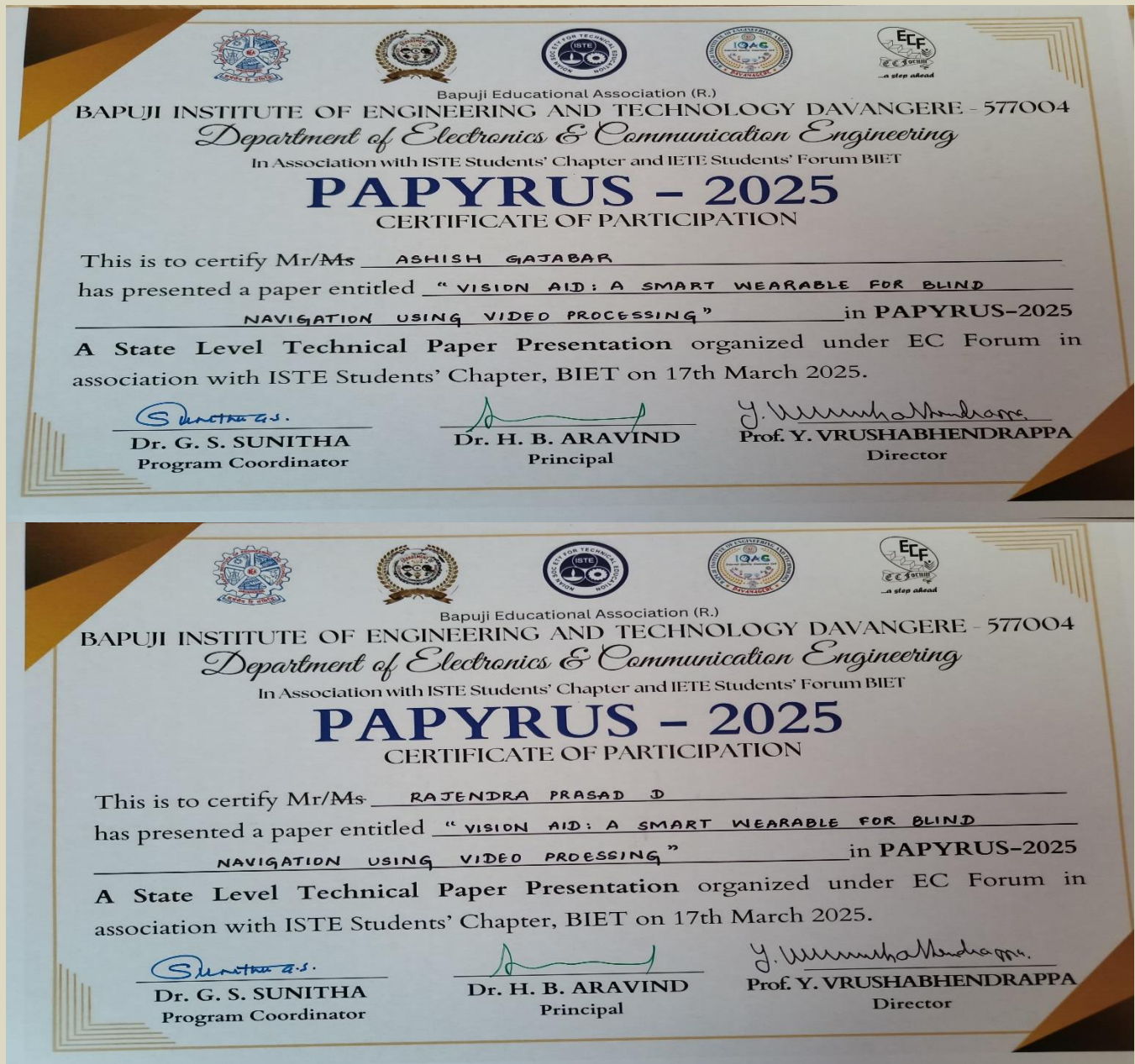
The visit included a comprehensive review of their academic processes, infrastructure, faculty preparedness and documentation in alignment with NBA requirements. His observations/remarks followed with critical evaluation helped them to enhance their accreditation readiness.

Student's Participation



Abhishek A and **Bhavana Gubber** from the 6th semester of the Electronics and Communication Engineering Department participated in the Inter-collegiate fest **UTKARSH 2025**, held at University B.D.T. College of Engineering, Davanagere, on **20th March 2025**, and received certificates for their active participation in the quiz.

Student's Participation : Paper Presentation



Ashish Gajabar and Rajendra Prasad D from the 6th semester of the Electronics and Communication Engineering Department participated in **PAPYRUS**, a state-level technical paper presentation event held at BIET, Davangere, on **17th March 2025**. They presented their paper titled "**Vision AID: A Smart Wearable for Blind Navigation Using Video Processing**" and received certificates for their active participation.

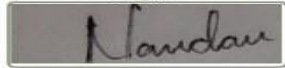
Student Achievement : Best Student Award



Images of best student award

Ayush G Y, 6th -semester student of the Electronics and Communication Engineering Department has been honoured with the **Best Student Award-2025** by the **Indian Society for Technical Education (ISTE)** for his outstanding academic performance and remarkable contributions to ISTE activities. The award was conferred during the **20th ISTE Karnataka State Level Student Convention 2025**, held at New Horizon College of Engineering, Bangalore, on **28th March 2025**. Hearty Congratulations and best wishes from College Management, Principal, HOD, Teaching, and non-teaching staff members on his well-deserved achievement.

Student Achievement : GATE Qualified

GRADUATE APTITUDE TEST IN ENGINEERING 2025 अभियांत्रिकी स्नातक अभिक्षमता परीक्षा २०२५ Organising Institute: INDIAN INSTITUTE OF TECHNOLOGY ROORKEE	
SCORE CARD	
Name of the Candidate	NANDAN PRAKASH P
Name of the Parent/Guardian	PRAKASH H
Registration No.	EC25S61332025
Date of Birth	May 3, 2004
Test Paper	Electronics and Communication Engineering (EC)
Date of Examination	February 15, 2025
GATE Score	256
Marks out of 100	17.0
All India Rank (AIR) in the test paper:	23982
Number of candidates appeared for the test paper:	81475
Qualifying Marks General: 25.0 EWS/OBC-NCL: 22.5 SC/ST/PwD: 16.6	
 	
 dc114d3a0ccab09bb9c5f80e5b4bbb3a	
A candidate is considered qualified if the marks secured are greater than or equal to the qualifying marks mentioned for the category, for which a valid category certificate, if applicable, must be produced along with this Score Card.	
This Score Card is valid up to 31st March 2028.	
GATE SCORE COMPUTATION The GATE 2025 score is calculated using the formula: $\text{GATE Score} = S_q + (S_t - S_q) \frac{(M - M_q)}{(M_t - M_q)}$ where, M is the marks obtained by the candidate in the test paper mentioned on the GATE 2025 Score Card M _q is the qualifying marks for general category candidates in the test paper M _t is the mean of marks of top 0.1% or top 10 (whichever is larger) of all the candidates who appeared in the test paper S _q = 350, is the score assigned to M _q , and S _t = 900, is the score assigned to M _t In the GATE 2025 score formula, the qualifying marks (M _q) for the general category candidate in each subject will be : Cut-off marks for GENERAL category = max(25, min(40, $\mu + \sigma$)). Here μ is the mean and σ is the standard deviation of positive marks of all the candidates who appeared in the test paper. Qualifying in GATE 2025 does not guarantee admission to a postgraduate program or scholarship/financial assistance. Admitting institutes may conduct additional tests or interviews for final selection of candidates. Graduate Aptitude Test in Engineering (GATE) 2025 was organised by Indian Institute of Technology Roorkee on behalf of National Coordination Board (NCB) - GATE for the Department of Higher Education, Ministry of Education (MoE), Government of India.	

Nandan Prakash P, 6th-semester student of the Electronics and Communication Engineering Department, appeared for the **GATE 2025 Examination** in the Electronics and Communication Engineering (EC) discipline, conducted by the **Indian Institute of Technology Roorkee**. He achieved a **GATE score of 256** and secured **17.0 marks out of 100**, with an **All-India Rank (AIR) of 23,982** out of 81,475 candidates. Hearty Congratulations and best wishes from College Management, Principal, HOD, Teaching, and non-teaching staff members on his achievement.

Faculty Outside World Interaction



Dr. Santosh Herur, Professor and Head, Dept. of E&CE and Director, IQAC of Jain Institute of Technology, Davangere has attended a **Board of Examiners (BoE)** meeting of PG Program - M.Tech(IoT and Sensor Systems) on **01/04/2025 (Tuesday)** in **Controller of Examination (CoE) Office, BIET**, Davangere.

EMERGE-2025:



JGI Jain Institute of Technology, Davangere
(A Unit of Jain Group of Institutions, Bengaluru)

In Association with ISTE Karnataka Section

Cordially invites you to the Inaugural function of
A State Level Technical Symposium
EMERGE - 2025
Empowering progress through technology...

CHIEF GUEST
Dr. Suresh D S
Director, CIT Group of Institutions
Chairman, ISTE, Karnataka Section

GUEST OF HONOR
Dr. Manjappa Sarathi
Director, Research and Consultancy
Sahyadri College of Engineering & Management, Mangaluru

SYMPOSIUM CHAIR
Dr. Ganesh D B
Principal & Director, JIT, Davangere

CONVENER
Dr. Santosh Herur
Professor and Head, Dept. of E&CE, JIT, Davangere

FACULTY COORDINATORS
Prof. Sharanabasaveshwara H B
Prof. Savitra C T
Dept. of E&CE, JIT, Davangere

STUDENT COORDINATORS
Mr. Ayush G Y
IEEE Student Secretary
Mr. Vinay S B
IEEE Student Chair

24 April 2025 9:30am
JIT Auditorium



JGI JAIN INSTITUTE OF TECHNOLOGY, DAVANAGERE
(A Unit of Jain Group of Institutions, Bengaluru)
Accredited by NBA, New Delhi | Accredited by NAAC with A+ Grade
Affiliated to VTU, Bengaluru | Approved by AICTE, New Delhi | Recognized by UGC under 201

Organizing
in Association with ISTE Karnataka Section

A State Level Technical Symposium

emerge 2025
Empowering progress through technology...

Sponsored By: **Abeyaantrix**

Events

- Tech Exchange
- Hopscotch
- Ideathon
- Emergence Hackathon
- E-Fashion Streak
- Tech Sketch
- Gizmo Guess
- Isotrix
- Techno Art
- Tech Cross Word Hunt

Win Attractive Prizes

<https://www.jitd.ac.in/emerge/2025/>

24 APRIL 2025 | **9:00 am** | **JIT Auditorium**

Faculty Coordinators:
Prof. Sharanabasaveshwara H B
Contact No : 8970416608
Prof. Savitra C T
Contact No : 807318236

Student Coordinators:
Mr. Ayush G Y (7022735247)
IEEE Student Secretary
Mr. Vinay S B (9380350542)
IEEE Student Chair

Register once & participate in 2 events
Registration fees
Rs. 100/- per head
Prof. Sunil M (97467663)
Treasurer

Dr. Santosh Herur
Prof. & Head, Dept. of E&CE
Convener

Dr. Manjappa Sarathi
Advisor
Jain Group of Institutions

Dr. Ganesh D B
Principal & Director
JIT, Davangere

Jain Institute of Technology, Davangere, is all set to organize **State level technical symposium** called **EMERGE-2025** on **April 24, 2025 at 9.30AM** in the College Auditorium. The event aims to showcase technical brilliance and encourage innovative thinking among aspiring engineers. The distinguished chief guest for the program will be **Dr. Suresh D S**, Director, CIT group of Institutions and Chairman, ISTE, Karnataka Section. The guest of Honor will be **Dr. Manjappa Sarathi**, Director, Research and Consultancy, Sahyadri College of Engineering and Management, Mangaluru. The Program coordinators for this technical symposium will be **Prof. Sharanabasaveshwara H B** and **Prof. Savitra C T** from Electronics and Communication Engineering department. Principal and Director of Jain Institute of Technology, **Dr. Ganesh D.B.**, along with Program Convener **Dr. Santosh Herur** expressed their excitement for the upcoming event.

EMERGE-2K25: A State Level Technical Symposium (24/04/2025)

INAUGURATION



Images of Emerge-2K25 Inauguration

EMERGE-2025, a State Level Technical Symposium, was inaugurated at the **JIT Auditorium** with a traditional lamp-lighting ceremony by the dignitaries. The event was graced by distinguished guests including **Dr. Suresh D S**, Director of CIT Group of Institutions and Chairman of ISTE Karnataka Section, as the **Chief Guest**, and **Dr. Manjappa Sarathi**, Director of Research and Consultancy at Sahyadri College of Engineering and Management, Mangaluru, as the **Guest of Honor**. The program commenced with an invocation song by **Ms. Anusha S P and team**, followed by a welcome address by **Ms. Harshitha V K**. **Dr. Santosh Herur**, Convener and HoD of the **Electronics & Communication Engineering Department**, gave insights about EMERGE-2025, and **Ms. Shaziya Siddiqua** introduced the Chief Guest. **Dr. Suresh D S** address to the gathered participants provided valuable insights about Technology, **Artificial Intelligence** in the present era. He Inspired the

student participants by the words to create their own footprints in this world and to continue pushing the boundaries of creativity and technical excellence in their professional career. **Dr. Manjappa Sarathi** emphasized the role of technology and shared key inputs on intellectual property rights and patents. **Dr. Ganesh D B, Principal and Director**, delivered the presidential remarks. Program coordinators **Prof. Sharanabasaveshwara H B** and **Prof. Savitra C T** were also present. The event concluded with a vote of thanks by **Ms. Kruthika G P**. Featuring 10 engaging technical events, EMERGE-2025 attracted enthusiastic participation from around 1200 students across various engineering colleges in Karnataka, making it a remarkable and impactful gathering of young minds.

Glimpses of various events Conducted:

Tech Exchange Event



Techno Art



Tech Sketch



Hackathon



Hopscotch



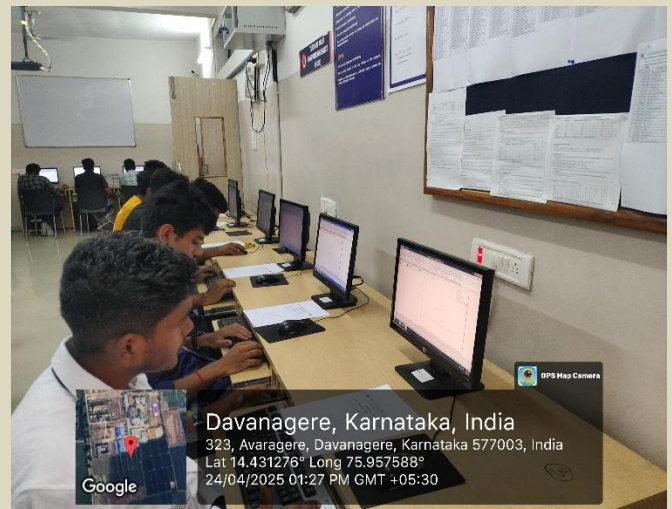
E-Fashion streak



Gizmo Guess



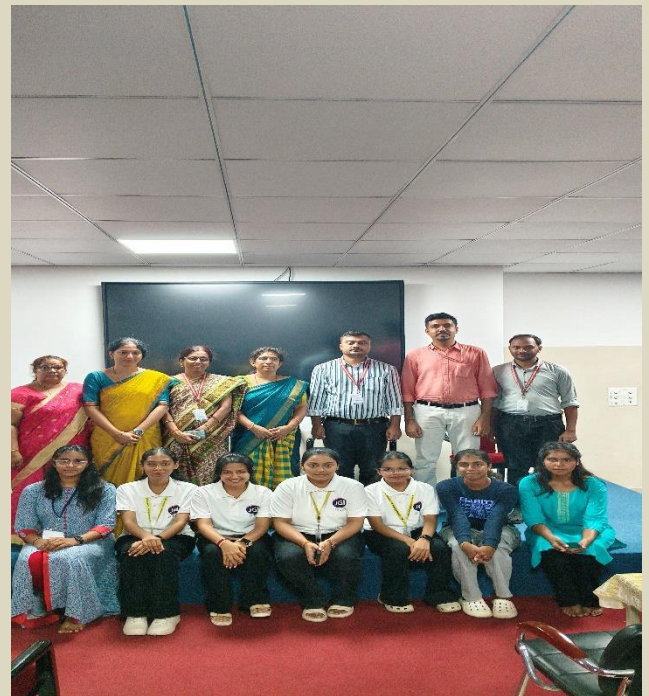
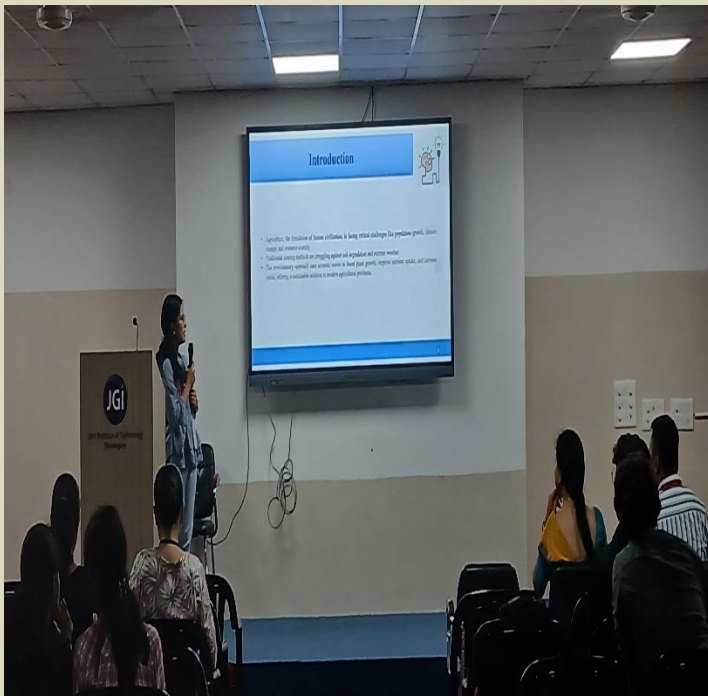
Isotrix



Tech Cross Word Hunt



Ideathon



Valedictory Ceremony of Emerge 2025



Images of Emerge-2K25 valedictory ceremony

Dr. Manjappa Sarathi, Director of Research and Consultancy at Sahyadri College of Engineering and Management, Mangaluru, was invited as the Chief Guest for the valedictory function. The program commenced with a warm welcome speech by Ms. Supriya P. **Dr. Santosh Herur**, the **Convener**, addressed the gathering, highlighting the significance of the event and appreciating the efforts of the participants. The guests felicitated the winners with prizes and shared motivational insights that encouraged students to pursue excellence. The function was graced by the presence of **Dr. Ganesh D B**, **Principal and Director**, **Dr. Santosh Herur**, **Convener**, along with **Prof. Sharanabasaveshwara H B** and **Prof. Savitra C T**, the Event Coordinators. The active participation of all staff members added to the vibrancy of the occasion. The event concluded with a heartfelt vote of thanks delivered by Ms. Chimayi D G, expressing gratitude to all who contributed to the success of the function.

EMERGE-2K25: Thanks giving ceremony(25/04/2025)



Images of Emerge-2K25 thanks giving ceremony

Emerge 2025 success meet and thanks giving ceremony was organised by the Department of Electronics & Communication Engineering. **Dr. Manjappa sarathi, Advisor, JITD** was invited as **chief guest** for the event. Certificates were distributed for all faculty coordinators and event coordinators. **Dr. Santosh Herur, Convener, Professor & HOD, Department of Electronics & Communication Engineering** addressed the gathering and shared the appreciation received from other institutions and colleagues regarding the successful conduction of Emerge 2025. He also thanked management and various departments for supporting for the successful conduction of the event. **Dr. Ganesh D B, Principal and Director, JITD** also addressed the gathering and congratulated the team of EMERGE 2025 for the successful conduction of the event. **Dr. Manjappa Sarathi, Advisor, JITD** also congratulated the team of JIT and Emerge and given his valuable feedback and suggestions. He also shared his best wishes for all the upcoming Events.

A Technical Talk On Voice Over IP -- 03/05/2025

JGI JAIN INSTITUTE OF TECHNOLOGY
(A Unit of Jain Group of Institutions, Bengaluru)
323, Near Veereshwara Punyashrama, Bada cross, Davangere- 577003
Accredited by NBA, New Delhi | Accredited by NAAC with A+ Grade
Affiliated to VTU, Belagavi | Approved by AICTE, New Delhi (Recognized by UGC under 20F)

A Technical Talk On
VOICE OVER IP
The Technology behind Modern Communication

Organized by
Department of Electronics & Communication Engineering

1. Ravindra G Palankar
Senior Technical Specialist
Avaya Technologies

2. Kavya Jain A
Engineer - Program Management
Avalon Technologies Ltd.

Coordinators
1. Dr. Ravikumar K. I
2. Prof. Nayana T

SATURDAY
03 MAY, 2025

CSE SEMINAR HALL
09.00 AM

*CS&EE&CEEEE, validity upto 30/06/2026

Dr. Santosh Herur
Professor & Head
Dept. of ECE, JIIT, Davangere

Dr. Manjappa S
Advisor
JIT, Davangere

Dr. Ganesh D B
Principal
JIT, Davangere



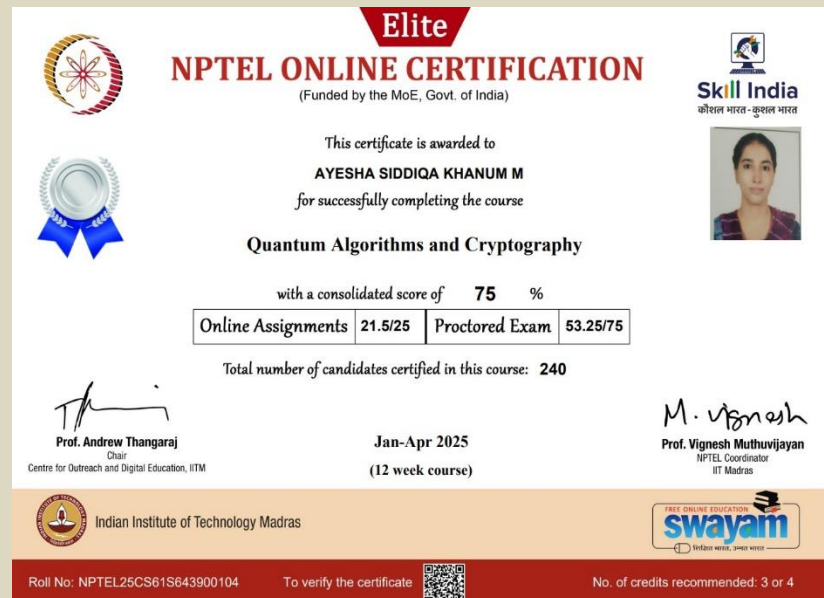
Images of technical talk

A Technical talk on “**Voice Over IP – The Technology Behind Modern Communication**” was organized by the Department of Electronics & Communication Engineering for 6th semester students on **3rd May 2025** at the CSE Seminar Hall. Alumni of JIITD **Mr. Ravindra G. Palankar**, Senior Technical Specialist at Avaya Technologies, and **Ms. Kavya Jain A**, Engineer – Program Management at Avalon Technologies Ltd. Were invited as guest speakers. **Dr. Santosh Herur**, Professor and Head, Dept. of E&CE graced the event with his presence. **Dr. Ravikumar K. I.**, Associate Professor, Dept. of E&CE and **Prof. Nayana T**, Assistant Professor, Dept of E&CE coordinated the event.

Mr. Ravindra G. Palankar in his session on voice over IP gave insights into the fundamentals of IP, voice communication over the internet and computer networks, and discussed industry applications and career opportunities in the field. **Ms. Kavya Jain A** addressed the students on electronic project development and marketing skills, and emphasized the importance of utilizing project development

methodologies for effective project planning, organization, and execution. The event proved to be highly informative and beneficial for the students, offering a blend of technical knowledge and practical industry exposure.

Faculty Achievement



The Department of Electronics and Communication Engineering proudly congratulates **Prof. Ayesha Siddiqua Khanum M** on successfully completing the prestigious **NPTEL course on Quantum Algorithms and Cryptography** with a commendable score of 75%, earning her the **Elite + Silver** certification. This remarkable achievement showcases her deep understanding of emerging technologies in quantum computing and cryptography. It stands as a testament to her unwavering dedication to academic growth, continuous professional development, and pursuit of excellence. Her success not only enriches her own knowledge but also adds immense value to the department's academic and research endeavors in advanced technological domains.

Faculty Achievement



Sl. No.	VGST Ref No.	Details of Principal Investigator	Title of the Project	Scheme	Subject	Funds to be released during the 1st year (Rs. in lakhs)	Funds to be released during the 2nd year (Rs. in lakhs)	Total Amount (Rs. in lakhs)
5	VRN/00089/23-24	Dr. Babu S Chavhan Assistant Professor, Department of Civil Engineering, Government Engineering College, Davanagi, Haveri - 581 110	Integrated Scientific Framework for the Study and Preservation of Historical Architectural Forms in India	GRE	Civil Engineering	25.00	15.00	40.00
	VRN/00465/23-24	Dr. Arjun U Associate Professor and HOD, Department of Computer Science and Engineering, PES Institute of Technology & Management, B'lore	Centre of Excellence in Blockchain- to create a Secure and Decentralized technology	GRE	Computer Science/Information Science	25.00	15.00	40.00
7	VRN/00150/23-24	Dr. Santosh Herur Professor & Head, Department of Electronics & Communication Engineering, Jain Institute of Technology, 323, Bada Cross, Near Blind School, Davanagere, Davanagere - 577 005	Development of a Universal FPGA-Based Embedded Development Board for Multi-Sector Applications Utilizing RISC-V Architecture	GRE	Electronics and Communication Engineering / Telecommunication Engineering	25.00	15.00	40.00
	VRN/00152/23-24	Dr. J. Lakshmi K. A. Associate Professor and Head, Department of Geology, Mangal Institute of Technology, Udipi - Karikala Road, Edapur Nagar, Mangal - 576 104	Groundwater Dynamics in Udipi District: An isotope based approach	GRE	Geology, Earth & Environmental Sciences	25.00	15.00	40.00
9	VRN/008153/23-24	Dr. Jaya Chetanyan K G Assistant Professor, Department of Mechanical Engineering, M. S. Ramiah Institute of Technology, Vidya Sadhana, HSR Nagar, HSRIT Post, Bengaluru - 560 054	Development of Sustainable Wire arc Additive manufacturing for refurbishing aerospace components	GRE	Mechanical Engineering	25.00	15.00	40.00
10	VRN/00860/23-24	Dr. Nagaraja Professor, Department of Applied Zoology, Kuvempu University, Jnanashayadri, Shankaragutta - 577 451, Bhadravathi Taluk, Shivamogga District	Molecular Genetic study of Siddi Tribal populations	GRE	Medical & Life Sciences	25.00	15.00	40.00
11	VRN/00854/23-24	Dr. Girish B.C. Associate Professor & Head, Department of Veterinary Pathology, Veterinary College, Gokula, Vidyanagar, Hassan - 573 202	Comparative transcriptomic analysis of cutaneous squamous cell carcinomas	GRE	Medical & Life Sciences	25.00	15.00	40.00

Page 2 of 3

Prof. Navakanta Bhat
Chairman, VGST Sub-Committee
HSC, Bengaluru - 560 012

Prof. C.N.R. Rao, F.R.S.
Chairman
Vision Group on Science and Tech
JNCASR, Jakkur Campus, Bengaluru-56

The Jain Institute of Technology, Davanagere, has been awarded a prestigious **₹40 lakh** grant by the **VGST**, Government of Karnataka under Grants for Research Excellence scheme for its project titled **“Development of a Universal FPGA-Based Embedded Development Board for Multi-Sector Applications Utilizing RISC-V Architecture.”** This innovative initiative, led by **Dr. Santosh Herur (Principal Investigator)**, **Dr. Santosh M. Nejakar (Co-Principal Investigator)**, and coordinated by **Mr. Sharanabasaveshwara H. B.**, aims to develop an open-source, reconfigurable platform using RISC-V and FPGA technologies. Designed to support affordable experimentation, prototyping, and deployment, the development board will serve diverse sectors such as healthcare, automotive, and IoT, while advancing self-reliant technological innovation in line with the Make-in-India initiative. Hosted at Jain Institute of Technology in collaboration with Silicon Microsystems, Bengaluru, the project marks a significant milestone in fostering cutting-edge research. The Management, **Dr. Ganesh D. B**, Principal and Director, Advisor **Dr. Manjappa Sarathi**, faculty, staff, and students have extended heartfelt congratulations to the entire project team for their commitment and efforts in securing this grant and enhancing the Institute’s research and innovation ecosystem.

Two Days Skill Development Program on “Microcontroller 8051 interfacing applications using Proteus Tool”-13th & 14th May – 4th ‘A’

JGI ARKA EDUCATIONAL & CULTURAL TRUST (REGD.)
JAIN INSTITUTE OF TECHNOLOGY
(A Unit of Jain Group of Institutions, Bengaluru)
323, Near Veereshwara Punyashrama, Bada cross, Davangere- 577003
Accredited by NBA, New Delhi* | Accredited by NAAC with A+ Grade
Affiliated to VTU, Belagavi | Approved by AICTE, New Delhi | Recognized by UGC under 2(f)

**A Two Days Skill Development Program On
Microcontroller 8051 Interfacing Applications
using Proteus Tool**

Organized by
Department of Electronics & Communication Engineering
In Association with
Abeyaantrix Softlabs

Target Audience : 4th Semester ‘A’ Section E&CE Students

Resource Persons
1. Mr. Shivaraj V S
Manager, Abeyaantrix Softlabs
2. Mr. Rakesh K B
Embedded Engineer
Abeyaantrix Softlabs

Coordinators
1. Prof. Anusha K
2. Prof. Nayana T

13th & 14th May 2025
09.30 AM - 04:30 PM
ECE Class Room : FF125

*CS&E,E&CE,EEE, validity upto 30/06/2026

Dr. Santosh Herur Convener
Professor & Head, Dept. of ECE
Dr. Manjappa S Advisor
JIT, Davangere
Dr. Ganesh D B Principal & Director
JIT, Davangere



Images of skill development program

A Two Days Skill Development Program (SDP) on “**Microcontroller 8051 interfacing applications using Proteus Tool**” was organized by Department of E&CE in association with Abeyaantrix softlabs, Davangere for the **4th semester ‘A’ section students from 13th to 14th May 2025** as a part of Course Activity. The Inauguration of SDP was organized at the Department of E&CE. **Mr. Shivaraj V S**, Manager, Abeyaantrix Softlabs and **Mr. Rakesh K B**, Embedded Engineer, Abeyaantrix Softlabs were invited as the resource persons for the SDP. **Dr. Santosh Herur**, Convener, Professor & Head, Dept. of E&CE presided over the event. Prof. Anusha K and Prof. Nayana T coordinated the Event. Resource Person, Mr. Shivaraj V S addressed the gathering and briefed about the interfacing and proteus tool. Dr. Santosh Herur, Convener, Professor & Head, Dept. of ECE addressed the students and gave insights about the importance of interfacing, its applications and explained the versatility of Proteus tool and its uses in project Development and also gave his best wishes for the successful conduction of the event.

Day 1 : Session – 1

In Session 1, Resouce Person, Mr. Rakesh K B explained basics of Microcontroller and its applications gave brief introduction to I/O Programming in Microcontroller 8051. He also explained C and Assembly programming for LCD and DAC interfacing, Concepts of Port Programming and Embedded C.



Day 1 : Session – 2

In Session 1, Resouce Person, Mr. Rakesh K B explained the steps for Proteus tool installation and its operation. The session also covered Hands-on practice : LCD & DAC Programming using Proteus software.



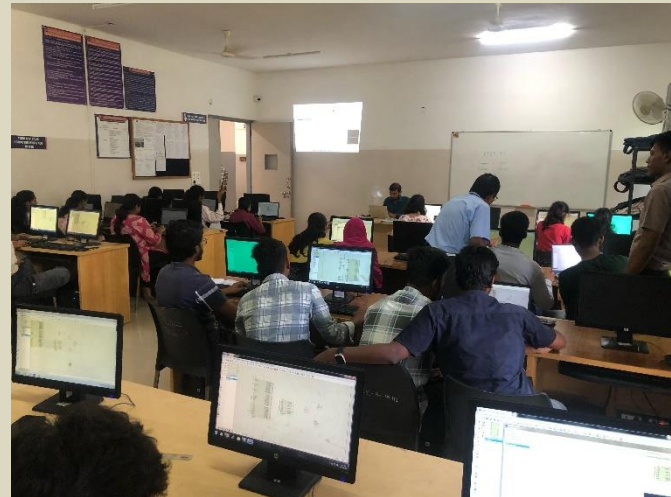
Day 2: Session – 3

In Session 3, Resource person, Mr. Rakesh K. B., explained ADC, Stepper motor, PWM, and DC motor programming. He also provided a functional overview and explained the interfacing techniques.



Day 2: Session – 4

In Session 4, Resource person, Mr. Rakesh K. B., conducted a hands-on practice session on ADC, stepper motor, PWM, and DC motor with practical implementation using the Proteus tool. The session also included Q&A, simulation testing, and a quiz.



Two Days Skill Development Program on “Microcontroller 8051 interfacing applications using Proteus Tool”- 19th & 20th May – 4th ‘B’

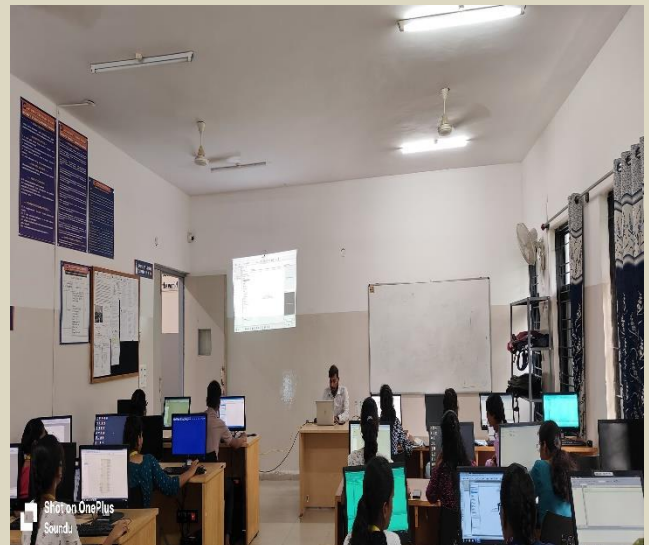
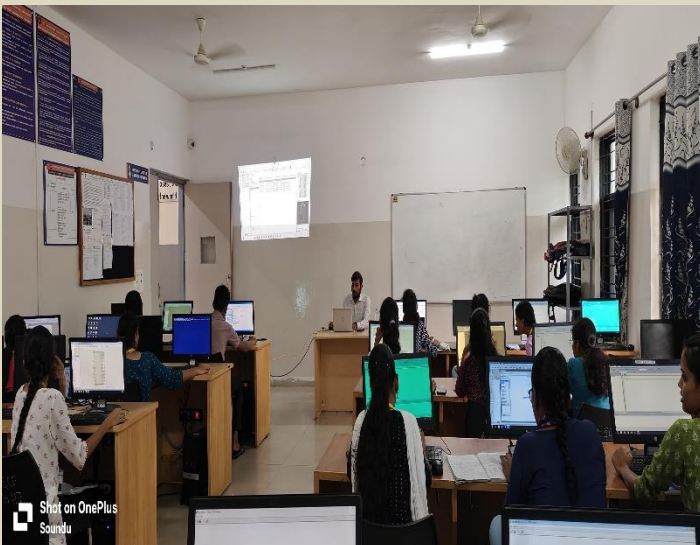
Day 1 : Session – 1

In Session 1, Resouce Person, Mr. Rakesh K B explained basics of Microcontroller and its applications gave brief introduction to I/O Programming in Microcontroller 8051. He also explained C and Assembly programming for LCD and DAC interfacing, Concepts of Port Programming and Embedded C.



Day 1 : Session – 2

In Session 1, Resouce Person, Mr. Rakesh K B explained the steps for Proteus tool installation and its operation. The session also covered Hands-on practice : LCD & DAC Programming using Proteus software.



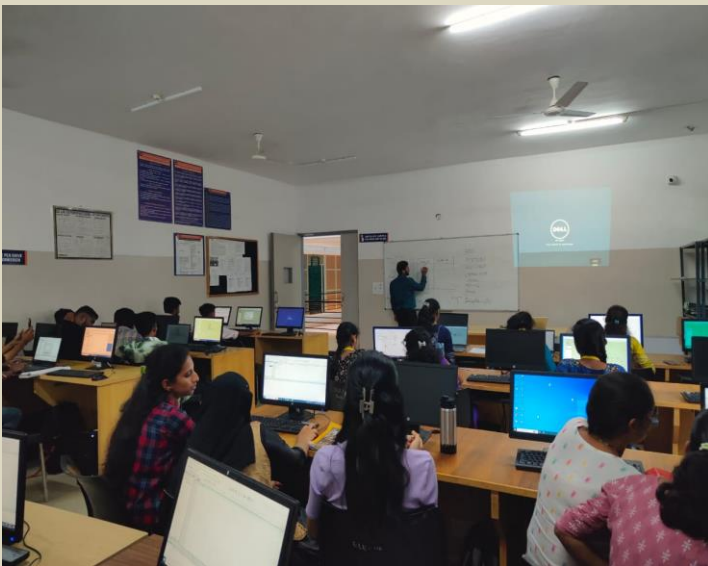
Day 2: Session – 3

In Session 3, Resource person, Mr. Rakesh K. B., explained ADC, Stepper motor, PWM, and DC motor programming. He also provided a functional overview and explained the interfacing techniques.



Day 2: Session – 4

In Session 4, Resource person, Mr. Rakesh K. B., conducted a hands-on practice session on ADC, stepper motor, PWM, and DC motor with practical implementation using the Proteus tool. The session also included Q&A, simulation testing, and a quiz.



Faculty Achievement



The Department of Electronics and Communication Engineering proudly congratulates **Prof. Ayesha Siddiqa Khanum M** on successfully completing the prestigious NPTEL courses: **Programming, Data Structures and Algorithms Using Python and Problem Solving Through Programming in C**, with a commendable score of **57%**, earning her official certification. This achievement reflects her strong grasp of foundational and emerging concepts in programming and algorithmic problem-solving. It is a testament to her unwavering commitment to academic excellence, continuous professional development, and a passion for lifelong learning.

**NPTEL ONLINE CERTIFICATION**
(Funded by the MoE, Govt. of India)

**Skill India**
कौशल भारत - कुशल भारत



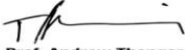
This certificate is awarded to
AYESHA SIDDIQA KHANUM M
for successfully completing the course

Programming, Data Structures And Algorithms Using Python

with a consolidated score of **57** %

Online Assignments	25/25	Proctored Exam	31.88/75
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Total number of candidates certified in this course: **1792**


Prof. Andrew Thangaraj
Chair
Centre for Outreach and Digital Education, IITM

Jan-Mar 2025
(8 week course)


Prof. Vignesh Muthuvijayan
NPTEL Coordinator
IIT Madras

 Indian Institute of Technology Madras



Roll No: NPTEL25CS59S336700044

To verify the certificate 

No. of credits recommended: 2 or 3

**NPTEL ONLINE CERTIFICATION**
(Funded by the MoE, Govt. of India)

**Skill India**
कौशल भारत - कुशल भारत



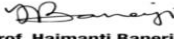
This certificate is awarded to
AYESHA SIDDIQA KHANUM M
for successfully completing the course

Problem Solving Through Programming In C

with a consolidated score of **57** %

Online Assignments	24.88/25	Proctored Exam	32.25/75
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Total number of candidates certified in this course: **2550**


Prof. Haimanti Banerji
Coordinator, NPTEL
IIT Kharagpur

Jan-Apr 2025
(12 week course)

 Indian Institute of Technology Kharagpur

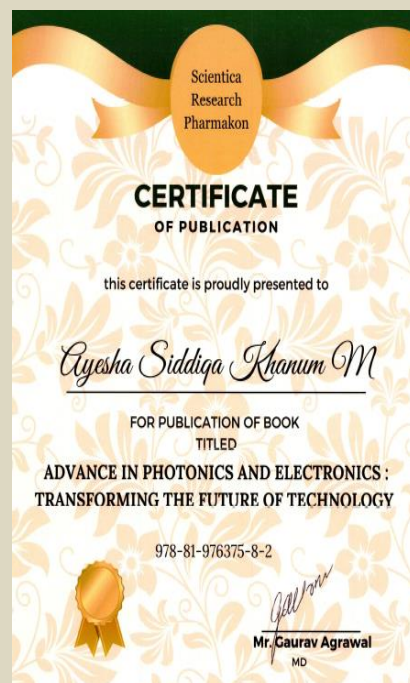
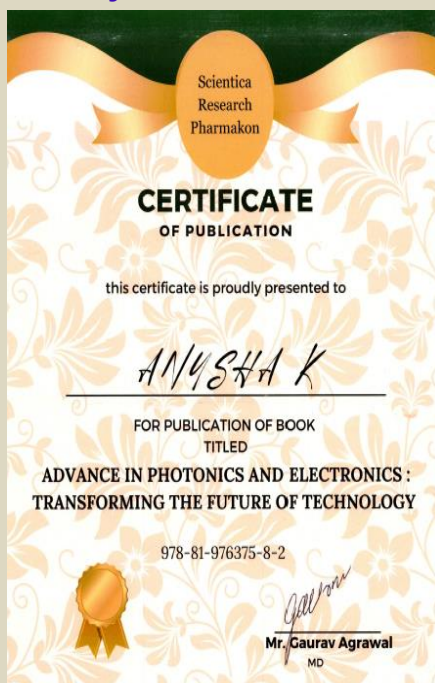
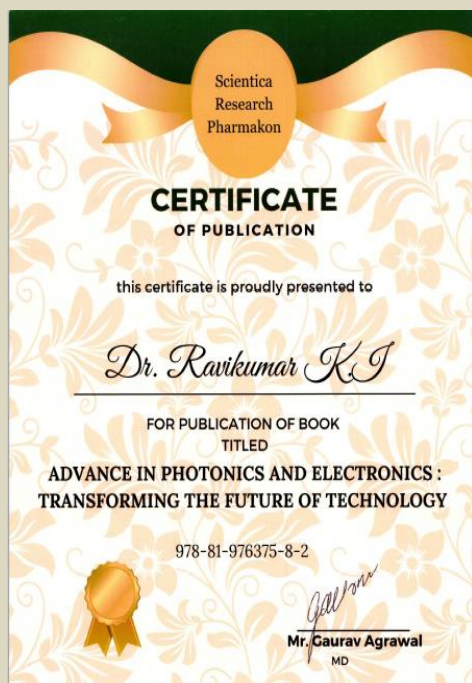


Roll No: NPTEL25CS56S543900103

To verify the certificate 

No. of credits recommended: 3 or 4

Faculty Achievement



Dr. Ravikumar K. I, Associate Professor and **Mrs. Anusha K** and **Ms. Ayesha Siddiqua Khanum**, Assistant Professor, Dept. of E&CE have published a book titled "**Advances in Photonics and Electronics : Transforming the future of Technology**" in Scientica Research Pharmakon publications. Hearty congratulations to the team on this accomplishment from College Management, Principal, HOD and all the staff members of E&CE Department.

Faculty Achievement

 **Electronics & ICT Academy**
Indian Institute of Technology Guwahati
An Initiative of Ministry of Electronics & Information
Technology (MeitY), Government of India

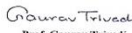
Ref No.: EICT/P-II/OF/24-25/004/078

Grade Certificate

Name of Participant : Mr. Sharanabasaveshwara H B
Father's Name : Bhuvaneshwara H
Mother's Name : Jayashree H B
Date of Birth : 29/02/1992
Course Name : Generative AI and DevOps
Duration : 17 - 22 February, 2025

Evaluation	Total Marks	Marks Obtained	Percentage
Theory	30	27	91%
Attendance	30	30	
Project	40	34	
Total	100	91	

Awarded Grade is "A"
(Refer overleaf for the grading formula)


Prof. Gaurav Trivedi
Principal Investigator

E&ICT Academy IIT Guwahati
Email: elctacad@iitg.ac.in, elctacad@gmail.com
Phone No: +91-361-258-3182/3199

Grading Formula

Percentage	Grades
Percentage > 80	Grade A
60 < Percentage ≤ 80	Grade B
Percentage ≤ 60	Grade C



E&ICT Academy IIT Guwahati
Email: elctacad@iitg.ac.in, elctacad@gmail.com
Phone No: +91-361-258-3182/3199

The Department of Electronics and Communication Engineering proudly congratulates **Prof. Sharanabasaveshwara H. B.** for successfully clearing the examination on **Generative AI and DevOps** conducted by **Indian Institute of Technology (IIT) Guwahati** as part of the **Faculty Development Program (FDP)** held at **BIET Davangere**. He has also been awarded a **Grade A Certificate** in recognition of his outstanding performance. This achievement reflects his dedication to continuous learning and excellence in emerging technologies.

Forum Valedictory and Farewell Ceremony



Images of forum valedictory and farewell

The Department of Electronics and Communication Engineering organized the valedictory function of its department forum, *Yaadein*, along with a farewell for the 2021–2025 batch students on 31st May 2025 at the JITD Auditorium. **Mr. Mallesh V. S.**, Technical Team Manager and Trainer at Abeyaantrix Softlab, Davangere, was invited as the Chief Guest. The event was presided over by **Dr. Manjappa S. Sarathi**, Advisor, JITD, **Dr. Ganesh D. B.**, Principal & Director, JITD, and **Dr. Santosh Herur**, Professor & Head, Department of E&CE. Faculty members and students were also present.

The program commenced with an invocation song by Ms. Anusha and team, followed by the inauguration ceremony where the dignitaries symbolically watered a plant, marking the beginning of the event. A warm welcome speech was delivered by Ms. Harshitha V. K.

Dr. Ganesh D. B., Principal & Director, encouraged students to believe in themselves and emphasized the importance of balancing academics and work life. **Dr. Santosh Herur** Professor & Head, Department of E&CE shared valuable insights about the current job market and extended his best wishes to the outgoing students.

Ms. Kruthika G. P. introduced the Chief Guest. **Mr. Mallesh V. S.**, who delivered an insightful speech focusing on current industry trends and career opportunities. He elaborated on emerging technologies such as automation, augmentation, and Artificial Intelligence (AI), explaining their growing influence across various domains. He highlighted how AI is revolutionizing even core sectors and encouraged students to focus on AI-enabled learning, practical exposure, and staying updated with industry developments. He concluded by wishing the students success in their future endeavors.

Prof. Sharanabasaveshwara H. B., Forum Coordinator, provided an overview of the diverse activities conducted by the forum throughout the academic year. Academic toppers of the odd semester (2024–2025) were awarded for their excellence and dedication. Students who excelled in various departmental events were also recognized, promoting a spirit of achievement and participation. The outgoing students shared their feedback and reflections, and the event concluded with a vote of thanks delivered by Ms. Srigowri G. B.

48th series of Student Project Programme: Selected for Project Exhibition



KARNATAKA STATE COUNCIL FOR SCIENCE AND TECHNOLOGY

Indian Institute of Science Campus, Bengaluru – 560 012
Website: www.kscst.org.in || Email: spp@kscst.org.in || Tel: 080-2334 1652, 2334 8848/49/40

48th series of Student Project Programme (SPP): 2024-25

List of Projects Selected for State Level Poster Presentation and Exhibition to be held at Jawaharlal Nehru New College of Engineering, Shivamogga on 1st and 2nd August 2025

59. JAIN INSTITUTE OF TECHNOLOGY, DAVANAGERE

Sl. No.	PROPOSAL REFERENCE NO.	PROJECT TITLE	COURSE	BRANCH	NAME OF THE GUIDE(S)	NAME OF THE STUDENT(S)	SELECTED FOR
188.	48S_BE_2257	SHESAFE+: MULTI-FEATURE PROTECTION FOR WOMEN AND DIFFERENTLY ABLED	B.E.	COMPUTER SCIENCE AND ENGINEERING	Prof. SHAFIULLA SHARIFF Prof. SAMEER B	Ms. SPANDANA A Mr. ANJAN RAJU HOVALE Ms. BHOMIKA P R Mr. PRAJWAL B R	Exhibition
189.	48S_BE_3847	LPG GAS LEAKAGE DETECTION AND AUTOMATIC SHUTDOWN SYSTEM WITH REAL TIME NOTIFICATION	B.E.	ELECTRONICS AND COMMUNICATION ENGINEERING	Mrs. ANUSHA T S	Ms. SINDHUSHREE M J Ms. HARSHITHA V M Mr. AMARANAİK L Mr. DARSHAN S	Exhibition

In the 48th series of Student Project Programme (SPP) 2024-25 organized by the Karnataka State Council for Science and Technology (KSCST), a team from the department of E&CE has been selected for State level project exhibition. The selected project, titled "**LPG Gas Leakage Detection and Automatic Shutdown System with Real-Time Notification,**" was guided by **Mrs. Anusha T S, Assistant Professor, and department of E&CE.** The project team comprises **Ms. Sindhushree M J, Ms. Harshitha V M, Mr. Amaranaik L, and Mr. Darshan S.** Hearty Congratulations to both the Project Guide and Students from College Management, Advisor, Principal, HOD and all the faculty members.

Student's Achievement : Placement-Keynes Technology



Bhumika S



Disha S Asundi



Darshan S

The Training and Placement Cell, JITD organized a placement drive for Keynes Technology on 29th May 2025, at JIT Auditorium for E&CE and EEE departments. From the E&CE department, three students were placed at Keynes technology. Hearty Congratulations to these students from College Management, Advisor, Principal, HOD, Placement Coordinators and all the faculty members.

Student's Achievement : Placement-SanDisk Technology



Sindhushree M J



The Training and Placement Cell, JITD, organized a placement drive for SanDisk Technology. From the E&CE department, Sindhushree M. J. was placed at SanDisk Technology. Hearty congratulations to this student from the College Management, Advisor, Principal, HOD, Placement Coordinators, and all the faculty members.

Faculty Achievement : Patent Publication



(12) PATENT APPLICATION PUBLICATION

(19) INDIA

(22) Date of filing of Application : 22/05/2025

(21) Application No. 202541049444 A

(43) Publication Date : 06/06/2025

(54) Title of the invention : AI-Enhanced User Behaviour Analysis and Targeting System for Social Media Advertising Using Deep Learning Models

(51) International classification : G06Q030025100, G06Q030020100, G06Q030020000, G06Q030024100, G06Q030000000

(86) International Application No

Filing Date

(87) International Publication No

(61) Patent of Addition to Application Number

Filing Date

(62) Divisional to Application Number

Filing Date

(71) Name of Applicant :

1) Dr. Ravikumar K.I

Address of Applicant : Associate Professor Department of Electronics and Communication Engineering Jain Institute of Technology No-323, Near Veereshwar Punysahram, BADA Cross, Avangere Village, Davangere - 577003 Karnataka India

2) Mrs. Mayuri Rohidas Londhe

3) Chiranjeevi Kondaiahini

4) Vijayashree C. Kalal

5) Mrs. R. Rajalakshmi

6) Mrs. P.C. Saranya

7) Mr. Anil Shrivastava

8) K. Rajkumar

9) Mr. K. Karthikayan

10) Sandeep Singh

Name of Applicant : NA

Address of Applicant : NA

(72) Name of Inventor :

1) Dr. Ravikumar K.I

Address of Applicant : Associate Professor Department of Electronics and Communication Engineering Jain Institute of Technology No-323, Near Veereshwar Punysahram, BADA Cross, Avangere Village, Davangere - 577003 Karnataka India

2) Mrs. Mayuri Rohidas Londhe

Address of Applicant : Assistant Professor Department of BBA-IB Sanjivani Arts, Commerce and Science College, Kopargaoon Ahilyanagar Maharashtra India

3) Chiranjeevi Kondaiahini

Address of Applicant : Assistant Professor, Department of Information Technology Vidya Jyothi Institute of Technology Asit Nagar, Hyderabad- 500075 Telangana India

4) Vijayashree C. Kalal

Address of Applicant : Assistant Professor Department of Electronics and Communication Engineering KLS Vishwanathra Deshpande Institute of Technology Udyog Vidya Nagar, Dandeli Road, Haloyal - 581329 Uttara Kannada Karnataka India

5) Mrs. R. Rajalakshmi

Address of Applicant : Assistant Professor Department of MBA Vels Institute of Science Tech and Advanced Studies Velu Nagar P.V. Vaidhyalingam Road Pallavaram Chennai - 600 117 Tamil Nadu India

6) Mrs. P.C. Saranya

Address of Applicant : Assistant Professor Department of MBA Vels Institute of Science Technology and Advanced Studies Velu Nagar P.V. Vaidhyalingam Road Pallavaram Chennai - 600 117 Tamil Nadu India

Dr. Ravikumar K.I, Associate Professor, Dept. of E&CE has successfully published a Patent titled **“AI-Enhanced User Behaviour Analysis and Targeting System for Social Media Advertising Using Deep Learning Models”**. Hearty congratulations to Dr. Ravikumar K.I on this accomplishment from College Management, Principal, HOD and all the staff members of E&CE Department.

Student's Achievement : Placement

JGI Jain Institute of Technology, Davangere
(Affiliated to VTU, Belagavi)

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Anusha C D

For Getting Selected in **SYRMA SGS**
CTC : 3.5 LPA

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD,
Placement Coordinators, Teaching & Non-Teaching Staffs and
Students.

9663868258 www.jitd.ac.in placement@jitd.in

JGI Jain Institute of Technology, Davangere
(Affiliated to VTU, Belagavi)

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Arfath Pasha A

For Getting Selected in **UDYOGI**
CTC : 3.5 LPA

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD,
Placement Coordinators, Teaching & Non-Teaching Staffs and
Students.

9663868258 www.jitd.ac.in placement@jitd.in

The Training and Placement Cell, JITD, organized a placement drive for Syrma SGS and Udyogi companies. From the E&CE Department, Anusha C. D. was placed at Syrma SGS Technology, and Arfath Pasha A. was placed at Udyogi. Hearty congratulations to these students from the College Management, Advisor, Principal, HOD, Placement Coordinators, and all the faculty members.

Student's Achievement : Placement

JGI Jain Institute of Technology, Davangere
(Affiliated to VTU, Belagavi)

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Ananya V

For Getting Selected in **@edutantr**
CTC : 4LPA

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD,
Placement Coordinators, Teaching & Non-Teaching Staffs and
Students.

9663868258 www.jitd.ac.in placement@jitd.in

The Training and Placement Cell, JITD, organized a placement drive for edutantr. From the E&CE department, Ananya V, was placed at edutantr. Hearty congratulations to this student from the College Management, Advisor, Principal, HOD, Placement Coordinators, and all the faculty members.

Comprehensive Viva at E&CE Department Research Centre



Images of comprehensive viva

Mr. Vikas Anandrao Patil bearing USN:4JD22PEC05, a research scholar candidate in the Department of Electronics and Communication Engineering Research Centre at Jain Institute of Technology, Davangere completed his Comprehensive Viva under the research supervision of **Dr. Santosh Herur**, Professor & Head, Department of E&CE, JIT, Davangere. The Comprehensive Viva was held on **11th JULY 2025** at **11:00AM** in the FF124 Classroom, E&CE Department.

The comprehensive viva was a significant milestone in Mr. Vikas Anandrao Patil's doctoral journey, where he presented his research progress and addressed his research inquiries. Research Advisory Committee members have given their inputs during this session which were invaluable in assessing his research work and his further research progress.

Student's Achievement : Placement

JGI Jain Institute of Technology, Davangere
(Affiliated to VTU, Belagavi)

JIT Placement
Congratulations

Sanjana K M

Branch : E&CE
Batch : 2024-25

For Getting Selected in **cognizant**
CTC: 4LPA

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD,
Placement Coordinators, Teaching & Non-Teaching Staffs and
Students.

9663868258 www.jitd.ac.in placement@jitd.in

The Training and Placement Cell, JITD, organized a placement drive for **Cognizant**. From the E&CE department, **Sanjana K M**, was placed at Cognizant. Hearty congratulations to this student from the College Management, Advisor, Principal, HOD, Placement Coordinators, and all the faculty members.

Placement Committee Meeting



The Department of Electronics and Communication Engineering conducted a Placement Committee meeting on 12th July 2025 at 10:30 AM. **Dr. Santosh Herur**, Professor and Head of the Department of ECE, **Dr. Prakash M. Walavalkar**, Dean of Training & Placement, and the counselors- **Mr. Sunil M.**, **Mrs. Deepthi G. B.**, and **Mrs. Anusha T. S.** for the upcoming 7th semester ECE students were present in the Dean's Training & Placement Chamber. The meeting focused on the upcoming Placement Training program by *Seventh Sense Talent Solutions*, scheduled to begin on **21st July 2025**. Counselors were instructed to closely monitor the attendance and ensure that all students attend every session without fail. Following the training, counselors are responsible for ensuring students continue to practice the skills learned by organizing mock interviews through the Students' Forum at the department level. It was also decided that if any student misses any part of the training or placement opportunities, their parents must be informed immediately. To keep parents informed and engaged, it was resolved to add them to their respective ward's counseling batch WhatsApp groups.

Student's Achievement : Placement -Academor, Atandra krykard, TAP Academy and Qspiders

JGI Jain Institute of Technology, Davangere (Affiliated to VTU, Belagavi) **NAAC GRADE A**

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Bhumika S

For Getting Selected in **Academor**

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD, Placement Coordinators, Teaching & Non-Teaching Staffs and Students.

9663868258 www.jitd.ac.in placement@jitd.in

JGI Jain Institute of Technology, Davangere (Affiliated to VTU, Belagavi) **NAAC GRADE A**

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Sheetal M S

For Getting Selected in **Academor**

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD, Placement Coordinators, Teaching & Non-Teaching Staffs and Students.

9663868258 www.jitd.ac.in placement@jitd.in

JGI Jain Institute of Technology, Davangere (Affiliated to VTU, Belagavi) **NAAC GRADE A**

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Varun Kumar B V

For Getting Selected in **Academor**

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD, Placement Coordinators, Teaching & Non-Teaching Staffs and Students.

9663868258 www.jitd.ac.in placement@jitd.in

JGI Jain Institute of Technology, Davangere (Affiliated to VTU, Belagavi) **NAAC GRADE A**

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Chandrika S

For Getting Selected in **Academor**

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD, Placement Coordinators, Teaching & Non-Teaching Staffs and Students.

9663868258 www.jitd.ac.in placement@jitd.in

JGI Jain Institute of Technology, Davangere (Affiliated to VTU, Belagavi)

JIT Placement
Congratulations



Batch : 2024-25
Branch : E&CE

Varun Kumar B V Disha S Asundi

For Getting Selected in

atandra KRYKARD CTC : 4.1 LPA

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD, Placement Coordinators, Teaching & Non-Teaching Staffs and Students.

9663868258 www.jitd.ac.in placement@jitd.in

JGI Jain Institute of Technology, Davangere (Affiliated to VTU, Belagavi)

JIT Placement
Congratulations



Branch : E&CE
Batch : 2024-25

Samreen Banu

For Getting Selected in **TAP ACADEMY**

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD, Placement Coordinators, Teaching & Non-Teaching Staffs and Students.

9663868258 www.jitd.ac.in placement@jitd.in



The Training and Placement Cell, JITD, organized a placement drive for Academor, Atandra Krykard, TAP Academy, and QSpiders. From the E&CE Department, Bhumika S., Sheetal M.S., Varun Kumar B.V., and Chandrika S. were placed at Academor. Varun Kumar B.V. and Disha S. Asundi were placed at Atandra Krykard. Samreen Banu was placed at TAP Academy, and Sheetal M.S. was also placed at QSpiders.

Hearty congratulations to these students from the College Management, Advisor, Principal, HOD, Placement Coordinators, and all the faculty members.

Placement Training



JGI Arka Educational and cultural trust Regd.
Jain Institute of Technology, Davanagere
 (A unit of Jain Group of Institutions, Bengaluru)
 Affiliated to VTU Belagavi | Approved by AICTE, New Delhi
 [Recognized by UGC under 2(f)] Recognized by Govt. Of Karnataka

Training and Placement Cell
Placement Training
 (Advanced Aptitude Training)

Phase II - 30 hours

21/07/2025 to 25/07/2025
9 am to 5 pm

For 7th Sem Engineering Students
Batch:2022-2026

Dr. Prakash M Walavalkar
 Dean (Training & Placement)

Dr. Ganesh D B
 Principal & Director

In association with
Seventh Sense Talent Solutions

Archana K N Vinutha M
 Event Coordinator

NAAC A+



The Department of Electronics and Communication Engineering, in collaboration with the Training and Placement Cell, JITD, organized a **Phase-2 Placement Training Program** in association with **Seventh Sense Talent Solutions, Bengaluru**, from **21st July to 25th July 2025**. This initiative aimed to enhance the employability skills of Seventh Semester ECE students by providing focused training on aptitude and communication skills.

On the training sessions covered a range of important topics including **Idioms and Phrases, Pipes and Cisterns, Cloze Test**, and **Statement and Analysis**. These sessions were designed to strengthen both verbal and quantitative reasoning abilities, which are crucial for cracking placement assessments and interviews.

Faculty Achievement



Dr. Ravikumar K.I, Associate Professor, Department of Electronics and Communication Engineering, has been honored with a *Certificate of Appreciation* by the **IEEE Bangalore Section** for his valuable contribution as a *Reviewer* at the **11th International Conference on Electronics, Computing and Communication Technologies (IEEE CONECCT 2025)**, held from **July 10-13, 2025** at Sterling's Mac Hotel, Bangalore. This prestigious recognition is a testament to Dr. Ravikumar's commitment to academic excellence and his active involvement in advancing research and innovation in the field of Electronics and Communication Engineering. Hearty congratulations to Dr. Ravikumar K.I on this accomplishment from College Management, Principal, HOD and all the staff members of E&CE Department.

48th series of Student Project Programme: State-Level Poster Presentation and Project Exhibition



In the 48th Series of the Student Project Programme (SPP) 2024-25, organized by the Karnataka State Council for Science and Technology (KSCST), Bengaluru, a team from the Department of Electronics and Communication Engineering (E&CE) participated in the state-level poster presentation and project exhibition held at JNNCE, Shivamogga, on 1st and 2nd August 2025. The project, titled "**LPG Gas Leakage Detection and Automatic Shutdown System with Real-Time Notification**," was guided by **Mrs. Anusha T S**, Assistant Professor, Department of E&CE. The project team comprised **Ms. Sindhushree M J**, **Ms. Harshitha V M**, and **Mr. Darshan S**. Hearty congratulations to the Project Guide and the student team from the College Management, Advisor, Principal, Head of the Department, and all the faculty members.

Faculty Achievement



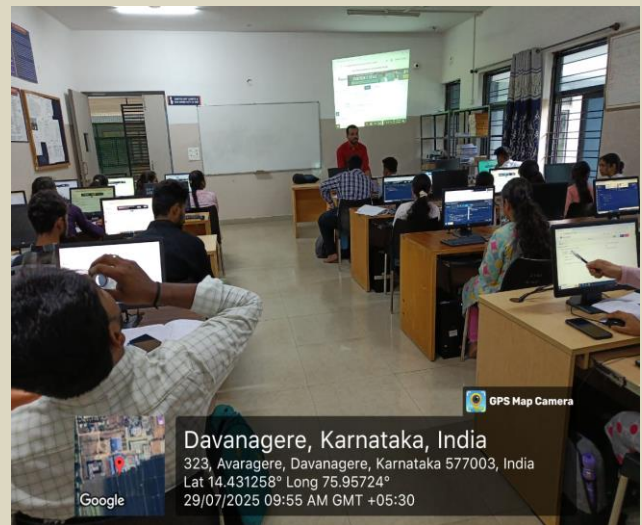
Dr. Ravikumar K.I, Associate Professor, Department of Electronics and Communication Engineering, has been conferred with a **Certificate of Appreciation by Springer** for his participation in the **4th International Conference on 6G Communications, Networking, and Signal Processing (SGCNSP 2025)**, organized by **Sri Krishna Institute of Technology, Bengaluru**, held on **27th and 28th June 2025**. He received this recognition for presenting his paper titled, **“Comprehensive Research on Visible Light Communication: Innovation and Use Cases.”** Hearty congratulations to Dr. Ravikumar K.I on this accomplishment from College Management, Principal, HOD and all the staff members of E&CE Department.

48th series of Student Project Programme: State-Level Poster Presentation and Project Exhibition



In the 48th Series of the Student Project Programme (SPP) 2024–25, organized by the Karnataka State Council for Science and Technology (KSCST), Bengaluru, a team from the Department of Electronics and Communication Engineering (E&CE) participated in the state-level poster presentation and project exhibition, held at Jawaharlal Nehru National College of Engineering (JNNCE), Shivamogga, on 1st and 2nd August 2025. The team was selected for the exhibition and awarded a Certificate of Participation for their project titled “LPG Gas Leakage Detection and Automatic Shutdown System with Real-Time Notification.” The project was guided by Mrs. Anusha T. S., Assistant Professor, Department of E&CE. The project team comprised Ms. Sindhushree M J, Ms. Harshitha V M, Mr. Amaranaik L and Mr. Darshan S. Hearty congratulations to the Project Guide and the student team from the College Management, Advisor, Principal, Head of the Department, and all the faculty members.

Phase-3 Placement Training: 28/07/2025 to 02/08/2025



The Department of Electronics and Communication Engineering, in collaboration with the Training and Placement Cell, JITD, organized **Phase-3 Placement Training Program on Advanced Technical Training** in association with Seventh Sense Talent Solutions, Bengaluru, from 28th July to 2nd August 2025. The program focused on strengthening students' Java basics and program execution through an online platform, thereby enhancing their skills for excelling in campus placements and corporate settings. On the final day, expert trainers delivered interactive sessions where students actively engaged in reviewing the concepts covered during the previous five days. This was followed by a hands-on session and an assessment conducted by the team comprising **Mr. Chandan, Mr. Jagdish, Mr. Bavith, Mr. Parshuram, Mr. Chirag, and Mr. Sartak**. The content was carefully designed to reinforce students' understanding of fundamental concepts and elevate their performance in advanced and dynamic programming sessions.

Mock Audit on Academic, Administrative, and NBA Compliance



An Academic and Administrative Audit, along with an NBA Compliance Audit, was conducted in the Department of Electronics and Communication Engineering on 7th August 2025. Audit Experts **Dr. Santosh Herur, Director, IQAC**, **Dr. Santosh M. Nejakar**, and **Dr. Latha B. M.** were invited to carry out the audit process. They thoroughly reviewed all the required documentation related to Academic and NBA Compliance at the department level and provided valuable feedback, suggestions, and observations aimed at enhancing the quality of documentation and presentation. The audit was conducted in the presence of **Dr. Ravikumar K. I., Accreditation Coordinator** and **Dr. Rajendra Soloni, IQAC Coordinator**, along with all teaching and non-teaching staff members of the department. The audit session served as a constructive step towards continuous improvement and preparedness for Academic and administrative and NBA Compliance.

Faculty outside world Interaction: NBA External Audit Experts



Dr. Santosh Herur, Professor & Head, Department of E&CE, Director, IQAC, Jain Institute of Technology, Davangere and **Dr. Rajendra Soloni**, Associate Professor, Department of E&CE and NBA Coordinator, Jain Institute of Technology, Davangere served as NBA Evaluation External Expert Members during the Mock NBA Accreditation Visit in the Department of E&CE at **UBDTCE**, Davangere on **14/08/2025**. The observations, suggestions and evaluations made by them about various criteria of the NBA were discussed with **Dr. Manjanaik**, Chairman, Department of E&CE, UBDTCE, Davangere which enhances the level of preparation for their actual NBA Peer Team Visit which is scheduled on 22nd, 23rd and 24th August 2025.

Placement Drive: Johnson Controls



Johnson Controls conducted a Technical Test and PPT session for all eligible 7th - semester students of Electronics and Communication Engineering, Electrical and Electronics Engineering, and Mechanical Engineering at Jain Institute of Technology, Davangere, on **21st August 2025**, from 12:00 PM to 2:00 PM. A total of 51 students attended from the Electronics and Communication Engineering department. Prof. Sunil M, Placement Coordinator, Department of ECE, communicated with all the students regarding the Technical Test from Johnson Controls.

Student's Achievement : Best Paper Presentation Award



Mr. Manoj S. and **Mr. Manoj K. J.**, 7th - semester students of Electronics and Communication Engineering at Jain Institute of Technology, Davanagere, participated in the **National Conference NCSEEE 2025**, held at GMU, Davanagere, on 20th August 2025. Under the guidance of **Mrs. Savitra C. T.**, Assistant Professor, Department of E&CE, they were awarded the **Best Paper Award** for their paper titled *"Image Processing in CT Scan and Quality Assessment"*. Hearty Congratulations and best wishes from College Management, Principal, HOD, Teaching, and non-teaching staff members on his well-deserved achievement.

JITD Signs MoU with Silicon Microsystem for VGST-Sponsored Research Project



Jain Institute of Technology, Davangere, signed a Memorandum of Understanding (MoU) with Silicon Microsystem to collaborate on the VGST-sponsored project titled *“Development of a Universal FPGA-Based Embedded Development Board for Multi-Sector Applications Utilizing RISC-V Architecture.”* On 22nd August 2025. The project is supported under the **Grants for Research Excellence** scheme of the **Vision Group on Science and Technology (VGST)** and the **Department of IT, BT and S&T, Government of Karnataka**. This initiative aims to strengthen research capabilities in advanced embedded systems and foster innovation in multi-sector applications. Speaking on the occasion, representatives from both institutions highlighted that the partnership would enhance research infrastructure, promote industry-academia collaboration, and provide students with exposure to cutting-edge technologies. This MoU marks a significant step in advancing technological research and development in Karnataka, furthering opportunities for innovation and skill development in the field of embedded systems. The MoU signing was graced by **Principal & Director Dr. Ganesh D. B, Dr. Manjappa S, Dr. Santosh Herur, Mr. Jayachandra Aradhya and Mr. Sharanabasaveshwara H B**. Principal investigator Dr. Santosh Herur expressed that this collaboration will significantly strengthen research infrastructure, promote industry-academia partnerships, and enable students and faculty to work with advanced technologies in embedded systems and RISC-V architecture.

A Technical Talk on “DevOps: Build, Test and Deploy Faster”



Arka Educational & Cultural Trust (Regd.)
Jain Institute of Technology, Davangere
(A Unit of Jain Group of Institutions, Bangalore)



Department of Electronics and Communication Engineering

ORGANIZING

A Technical Talk on
“DevOps: Build, Test and Deploy Faster”

Resource Person
Mr. Vikas Yatnalli
Lead DevOps Engineer, Publicis Sapient



Date : 23/08/2025 Time : 10.15 am to 12.15 p.m Venue : EEE Seminar Hall

Forum Coordinator Mr. Sharanabasaveshwara H B	Professor & Head Dr. Santosh Herur	Principal Dr. Ganesh D B
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Images of Technical talk

The Department of Electronics & Communication Engineering, Jain Institute of Technology, Davangere, organized a **Technical Talk on “DevOps: Build, Test and Deploy Faster”** on 23rd August 2025, from 10:15 AM to 12:30 PM. The session aimed to equip students with knowledge of modern software development and deployment practices. The resource person for the session was **Mr. Vikas Yatnalli, Lead DevOps Engineer at Publicis Sapient**, who delivered an engaging and insightful talk. During the session, Mr. Vikas Yatnalli explained the **importance of DevOps in bridging the gap between development and operations**, enabling faster delivery of high-quality software. He introduced students to **key DevOps practices** such as Continuous Integration (CI), Continuous Deployment (CD), automated testing, configuration management, and monitoring. He also highlighted the role of **popular tools like Git, Jenkins, Docker, Kubernetes, and cloud platforms** in today’s DevOps workflows. Students gained practical insights into how DevOps improves **collaboration, scalability, and efficiency in real-world projects**. The session emphasized the growing demand for DevOps professionals in the IT industry and encouraged students to develop hands-on skills in this emerging domain. The event was well-received by both

students and faculty members, who noted that such industry-oriented sessions help in enhancing employability skills and keeping pace with rapidly evolving technology trends. The event was coordinated by **Prof. Sharanabasaveshwara H. B.**, Assistant Professor, Dept. of ECE, and **Dr. Santosh Herur**, Professor & Head, Dept. of ECE, who conveyed the Technical Talk. A total of **72 students from the 5th Semester** attended the program, along with all the faculty members of the E&CE Department.

Odd Semester Results of 2024-2025

Sl no.	Semester	Overall Result %
1	III	59/134==44.02
2	V	48/67==71.64
3	VII	56/69=81.57

Topper's List

3 RD Semester	USN	Name	Marks_Scored	Percentage %
First	4JD23EC103	Soumya Mallikarjun Kumbar	671/800	83.88
Second	4JD23EC050	Lisha G L	659/800	82.37
Third	4JD23EC031	Ganesh S	641/800	80.12

5 TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD22EC053	Soumya Manjunath Madivalar	681/800	85.13
Second	4JD22EC009	Ayush G Y	677/800	84.63
Third	4JD22EC005	Amulya U	662/800	82.75

7 TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD21EC049	SINDHUSHREE D M	594/700	84.86
Second	4JD21EC011	BHUMIKA S	591/700	84.43
Third	4JD21EC030	NIKITHA S M	578/700	82.57

Even Semester Results of 2024-2025

Sl. no.	Semester	Overall Result %
1	IV	69/134= 51.45
2	VI	62/67=92.53
3	VIII	69/69=100

Topper's List

4 TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD23EC103	Soumya Mallikarjun Kumbar	630/800	78.75
Second	4JD23EC073	Priyanka Palakshi Hunasikatti	623/800	77.87
Third	4JD23EC050	Lisha G L	610/800	76.25

6 TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD22EC009	AYUSH GY	631/700	90.14
Second	4JD22EC053	SOUMYA MANJUNATH MADIVALAR	630/700	90
Third	4JD22EC020	FATHIMA MIZBA	624/700	89.14

8 TH Semester	USN	Name	Marks_Scored	Percentage %
First	4JD21EC035	PRIYA R	394/400	98.5
Second	4JD21EC011	BHUMIKA S	392/400	98
Third	4JD21EC048	SINDHUSHREE M J	391/400	97.75