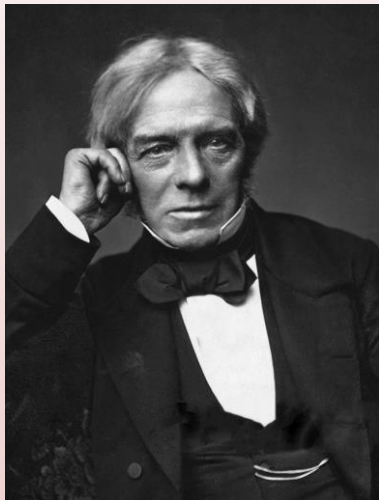


Newsletter

SEPTEMBER
2025

"Nothing is too wonderful to be true, if it be consistent with the laws of nature."



"The important thing is to never stop questioning."

Michael
Faraday



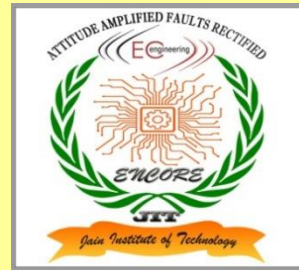
ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davangere

(A Unit of Jain Group of Institutions, Bangalore)



*Department of Electronics and
Communication Engineering*



Electro Infinity

Create, Enhance and Sustain



About the Department



Dr. Santosh Herur
Professor & Head
Dept. of E&CE
JIT, Davangere

Department of Electronics and Communication was started in the year 2011-12. The department emphasizes on imparting advanced technical knowledge in various fields like signal processing, VLSI Technology, Embedded system, wireless technology, microwave engineering, optical communication, and microelectronics and control systems. Highly qualified, experienced and dedicated staff members are the backbone of the department. Well equipped, sophisticated, precision and state of the art laboratories are centre of attraction in the campus. Another major strength of the department is its extensive collection of technical books, periodical and journals, ENCORE association of the department of E & CE aims at targeting innovations in the field of advanced communication and networking.

The Department has MOU with Karunadu Technologies Pvt. Ltd, Bengaluru, Abiyantrix, Soft Solutions, Davangere, Sai Tech, Mysore and PSN Hub IT consultancy services. The MOU organizations provide Internship training and conduct workshops on recent trends and organize seminars for the students to enhance their industrial exposure.

Editor's desk



Ms NAYANA T

*Assistant Professor
Dept. of E&CE,
IIT, Davangere*

*Warm greetings to the readers of **Electro Infinity**!!*

We are pleased to share with you the September issue of the newsletter for the year 2025 with a Heartfelt wish of Teachers Day and Eid-E-Milad. We express our considerable gratitude and appreciation to everyone who have contributed to this Newsletter magazine. These contributions have required a generous amount of time and effort. It is this willingness to share knowledge, concerns and special insights with fellow beings that has made this magazine possible.

Finally we would like to conclude with these words "Science is a wonderful thing if one does not have to earn one's living at it" - Michael Faraday.

Happy reading

Thank you

NAYANA T

Vision of the Department

Endeavours to achieve academic excellence by providing value based education to the students of Electronics and communication engineering and enable them to meet the ever changing needs of the society.

Mission of the Department

- ❖ *To emphasize on student focus learning environment.*
- ❖ *To prepare graduates who surpass in the engineering career, qualified to acquire technical knowledge and ethical values.*
- ❖ *To provide a platform to inculcate team spirit and leadership qualities to succeed in professional career.*

Program Specific Outcomes (PSOs)

- ❖ *PSO1: Analyze, simulate and design electronic and communication circuits for given specifications.*
- ❖ *PSO2: Design and test processor based systems*

Program Educational Objectives (PEOs)

- ❖ *PEO1: To create graduates with a strong base in Electronics & Communication Engineering.*
- ❖ *PEO2: To produce technically competent graduates with the ability to understand, analyse & develop electronic systems.*
- ❖ *PEO3: Ability to work with professional ethics and as a team member to realize the goals of the projects.*
- ❖ *PEO4: Excellence in professional career by attaining higher education.*

Faculty Achievement : Paper Publication



Dr. Ravikumar K. I., Associate Professor in the Department of Electronics and Communication Engineering, has achieved a significant milestone by publishing a research paper titled **“Enhancement of Performance through Optimized Intelligent Static Random Access Memory Design – Soft Computing.”** This paper was officially published on 31st August 2025 in the prestigious **Springer journal**, marking a notable contribution to the field of VLSI and memory design.

The College Management, Principal, Head of the Department, and all faculty members of the Department of E&CE extend their heartfelt congratulations to Dr. Ravikumar K. I. on this outstanding accomplishment. His dedication to research and academic excellence continues to inspire students and colleagues alike, while also bringing pride and recognition to the institution.

Empowering Future Careers: Placement Talk with Dean & Coordinator



Dr. Prakash M. Walvalkar, Dean of the Training & Placement Cell, along with **Mr. Sunil M.**, Placement Coordinator of the Department of ECE, addressed the seventh-semester students of the ECE Department in a special Placement Talk focusing on upcoming opportunities. The session emphasized the importance of Superset registration for a smooth placement process, outlined the guidelines for the Placement Assessment Test, and highlighted key strategies for preparation towards recruitment drives.

Students were encouraged to actively participate, stay updated with placement notifications, and make the best use of the training resources provided by the institute. This initiative marks a significant step in guiding students towards successful careers.

Student's Achievement : Placement



JGI Jain Institute of Technology, Davangere
(Affiliated to VTU, Belagavi)

JIT Placement
Congratulations

Branch : E&CE
Batch : 2024-25

Amara Naik L

For Getting Selected in **CTC : 2.5 LPA**

Best Wishes From Management, Principal, Advisor, Dean(T&P), HOD,
Placement Coordinators, Teaching & Non-Teaching Staffs and
Students.

9663868258 www.jitd.ac.in placement@jitd.in



The Training and Placement Cell, JITD, organized a placement drive for Integrated Wireless Solutions. From the E&CE department, Amara Naik L, was placed at Integrated Wireless Solutions. Hearty congratulations to this student from the College Management, Advisor, Principal, HOD, Placement Coordinators, and all the faculty members.

JITD Tomorrow:



Arka Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davangere

(A Unit of Jain Group of Institutions, Bangalore)



Department of Electronics and Communication Engineering

ORGANIZING

A Alumni Talk and Interaction on

“Future Internship/Job Opportunities in Embedded Domain”

Resource Person

Mr. Rakshith Sawanth

Product Development Engineer, Schneider Electric



Date : 08/09/2025

Time : 10.15 am to 12.15 p.m

Venue : FF125

Forum Coordinator

Mr. Sharanabasaveshwara H B

Professor & Head

Dr. Santosh Herur

Principal

Dr. Ganesh D B

A Alumni Talk and Interaction on “Future Internship/Job Opportunities in Embedded Domain”



Arka Educational & Cultural Trust (Regd.)
Jain Institute of Technology, Davangere
(A Unit of Jain Group of Institutions, Bangalore)



Department of Electronics and Communication Engineering

ORGANIZING

A Alumni Talk and Interaction on
“Future Internship/Job Opportunities in Embedded Domain”

Resource Person
Mr. Rakshith
Product Development Engineer, Schneider Electric



Date : 08/09/2025 Time : 10.15 am to 12.15 p.m Venue : FF125

Forum Coordinator Mr. Sharanabasaveshwara H B	Professor & Head Dr. Santosh Herur	Principal Dr. Ganesh D B
--	---------------------------------------	-----------------------------



The Department of Electronics and Communication Engineering, Jain Institute of Technology, Davangere (JITD), organized an Alumni Interaction Session on 8th September 2025 focusing on the Embedded Systems domain. The Resource Person was **Mr. Rakshith Savanth**, Product Development Engineer, Schneider Electric, and proud alumnus of JITD.

Mr. Rakshith shared his rich industry experience in product development and embedded system applications. He highlighted the latest trends, challenges, and career opportunities in the embedded domain, guiding students on the essential skills required to excel in the field. The interactive session encouraged students to align their academic knowledge with industry practices and inspired them to pursue innovation-driven careers.

The program served as an excellent platform to strengthen the institute–alumni bond and motivated students to prepare themselves for emerging industry needs.

Four Days Skill Development Program on "Embedded system for Project/product development"

ARKA Educational & Cultural Trust (R)

JGI JAIN INSTITUTE OF TECHNOLOGY
(A UNIT OF JAIN GROUP OF INSTITUTIONS, BENGALURU)

NAAC GRADE A+ NEA IEEE BANGALORE SECTION IQAC

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

INVITES FOR THE INAUGURATION OF
"FOUR DAYS SKILL DEVELOPMENT PROGRAM"
on
EMBEDDED SYSTEM FOR PROJECT / PRODUCT DEVELOPMENT

RESOURCE PERSONS

Mr. Shivaraj V S Manager Abeyaantrix Edusoft LLP	Mr. Rakesh K B Embedded Engineer Abeyaantrix Edusoft LLP
Mrs. Deepthi G B Mr. Sharanabasaveshwara H B Coordinators	Dr. Santosh Herur Convener
Dr. K B Prakash Dean Academics	Mr. Praveen Anaji Skill Lab Coordinator

Targetted Audience : 5th Sem Students

17 Sept 2025
9.30 AM

ECE Dept
JIT

Dr. Manjappa Sarathi
Advisor

Dr. Ganesh D B
Principal & Director



A Four-Day Skill Development Program (SDP) on **"Embedded System for Project/Product Development"** was organized by the Department of E&CE in association with **Abeyaantrix Softlabs, Davanagere** for the 5th semester 'B' section students from **17th to 18th September 2025** at 10:00 AM in classroom FF125.

The Inauguration of SDP was organized at the Department of E&CE. **Mr. Shivaraj V. S., Manager, Abeyaantrix Softlabs,** and **Mr. Rakesh K. B., Embedded Engineer, Abeyaantrix Softlabs,** were invited as the resource persons for the program. **Dr. Santosh Herur, Convener, Professor & Head, Dept. of E&CE,** presided over the event. The program was coordinated by **Mrs. Deepthi G. B.** and **Mr. Sharanabasaveshwara H. B.** During the inauguration, **Dr. Santosh Herur** addressed the students about the importance of the workshop.

On the same day, a session on Arduino followed by a hands-on programming session was conducted. The students actively participated and gained practical exposure to programming, which helped them strengthen their fundamentals and boosted their confidence in applying embedded system concepts to real-time projects.

Four Days Skill Development Program on "Embedded system for Project/product development"

A Four-Day Skill Development Program (SDP) on “Embedded System for Project/Product Development” was organized by the Department of E&CE in association with **Abeyaantrix Softlabs, Davangere** for the 5th semester ‘B’ section students from **17th to 18th September 2025** at 10:00 AM in classroom **FF125**.



On the second day of the Skill Development Program, the resource person delivered a hands-on training session on interfacing LCD displays, analog and digital sensors, motors, and servo motors with the ESP32 microcontroller.

Dean Academics motivating students to enhance their skills through Honors Degree and online certification platforms



Dr. Prakash K. B., Dean Academics, addressed the 5th semester 'B' section students of the Electronics & Communication Engineering Department on 22nd September 2025 regarding the scope and benefits of pursuing an Honors degree alongside their regular curriculum. He emphasized the importance of utilizing platforms such as NPTEL, SWAYAM, and MOOCs for continuous learning and skill enhancement. He further highlighted how these courses provide value-added knowledge beyond classroom learning and help students align with industry requirements. The session also encouraged students to explore online certification programs to strengthen their academic and professional profiles.

Forum ENCORE Inauguration and Fresher's Day





The Inauguration Function of the E&CE Department **Forum ENCORE** and Fresher's Day '**Celestia 2025**' was organized on 26th September 2025 at the JITD Auditorium.

Mr. Sachin Kumar Patil, Assistant Manager, Technology & Innovation Learning Foundation, was invited as the chief guest for the event, Dr. Ganesh D. B, Principal and director, JITD presided over the event. Guest of Honour Dr. Manjappa Sarathi, Advisor, JITD. Dean Academics, Dr. K. B. Prakash, Dr. Santosh Herur, Professor and Head, Department of E&CE, Forum coordinator Prof. Sharanabasaveshwara H. B, Forum representatives, and Forum secretary and Treasurer Representatives also chaired the stage. The event got a great start by the Bharatanatyam performance by 5th semester student Priyanka B Araleshwar. All the dignitaries inaugurated the event by lighting the lamp.

Forum Coordinator, **Prof. Sharanabasaveshwara H. B.** gave a brief insight about the events conducted under Forum ENCORE and upcoming events planned to conduct for the benefit of students.

Dr. Santosh Herur, Professor and Head, Dept. of E&CE addressed the gathering and emphasized career opportunities and emerging technologies in E&CE. He also spoke about outcome-based education and NEP scheme and threw light on the accreditation and achievements of Institution and department. He welcomed all freshers to the department and encouraged students to join NPTEL, MOOC courses and upskill the knowledge through technical and placement activities. He also urged students to actively participate in technical seminars, conferences, symposium, sports and participate in all the forum and cultural activities.

Mr. Sachin Kumar Patil, addressed the students on the importance of research and innovation, its needs, and its future. He highlighted the emerging fields of E&CE, their challenges, and the skills required to be a successful ECE engineer.

Guest of Honour, **Dr. Manjappa Sarathi**, addressed students regarding upskilling knowledge in the core ECE Fields like VLSI, Analog and digital communication, Networking, Embedded systems and chip design to attract job opportunities in the field in coming days and he also congratulated and wished the best to students for all their endeavours.

Academic toppers were felicitated, and winners of extracurricular activities were awarded prizes.

Dr. K. B. Prakash, Dean Academics, JITD, congratulated the students and academic toppers. He advised students to use technology for creative and progressive engineering works and cautioned against using it for destructive purposes, sharing meaningful examples.

Principal and Director, JITD, **Dr. Ganesh D. B**, in his presidential remarks congratulated the academic toppers and wished all the best to all the freshers for their exciting journey as an ECE Engineers. He advised students to plan for their future after four years of engineering and upskill themselves to meet emerging trends in the field of ECE. He also guided them to prepare for GATE examinations and make the best use of the library resources available.

Student's Participation



Chinmayi L., Shubha B. K., Dhanusha K. J., and Deepika N. from the 5th semester of the Electronics and Communication Engineering Department participated in 'CODE VAULT 2025', a 24-hour National Level Hackathon organized by the Department of Computer Science and Business Systems, Maharaja Institute of Technology, Mysore, held from 19th to 20th September 2025. They received certificates for their active participation in the Hackathon.

Aayudha Pooja Celebration



On the occasion of the Dasara festival, the Department of Electronics and Communication Engineering celebrated Ayudha Pooja. Dr. Ganesh D. B., Principal and Director, JITD. Dean Academics, Dr. K. B. Prakash, Dr. Santosh Herur, Professor and Head, Department of E&CE, and all the faculty members of the E&CE Department were present during the celebration.