

Newsletter

OCTOBER
2025

"To succeed in
your mission, you
must have single-
minded devotion
to your goal."



"Great dreams
of great
dreamers are
always
transcended."

Dr. A.P.J.
Abdul Kalam



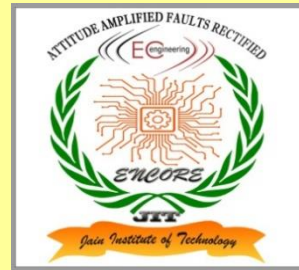
ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davangere

(A Unit of Jain Group of Institutions, Bangalore)



*Department of Electronics and
Communication Engineering*



Electro Infinity

Create, Enhance and Sustain



About the Department



Dr. Santosh Herur
Professor & Head
Dept. of E&CE
JIT, Davangere

Department of Electronics and Communication was started in the year 2011-12. The department emphasizes on imparting advanced technical knowledge in various fields like signal processing, VLSI Technology, Embedded system, wireless technology, microwave engineering, optical communication, and microelectronics and control systems. Highly qualified, experienced and dedicated staff members are the backbone of the department. Well equipped, sophisticated, precision and state of the art laboratories are centre of attraction in the campus. Another major strength of the department is its extensive collection of technical books, periodical and journals, ENCORE association of the department of E & CE aims at targeting innovations in the field of advanced communication and networking.

The Department has MOU with Karunadu Technologies Pvt. Ltd, Bengaluru, Abiyantrix, Soft Solutions, Davangere, Sai Tech, Mysore and PSN Hub IT consultancy services. The MOU organizations provide Internship training and conduct workshops on recent trends and organize seminars for the students to enhance their industrial exposure.

Editor's desk



Ms NAYANA T

*Assistant Professor
Dept. of E&CE,
JIT, Davanagere*

*Warm greetings to the readers of **Electro Infinity**!!*

We are pleased to share with you the October issue of the newsletter for the year 2025 with a Heartfelt wish of Dasara and Deepavali festival. We express our considerable gratitude and appreciation to everyone who have contributed to this Newsletter magazine. These contributions have required a generous amount of time and effort. It is this willingness to share knowledge, concerns and special insights with fellow beings that has made this magazine possible.

***Finally we would like to conclude with these words** Thinking should become your capital asset, no matter whatever ups and downs you come across in your life, Dr. A.P.J. Abdul Kalam*

Happy reading

Thank you

NAYANA T

Vision of the Department

Endeavours to achieve academic excellence by providing value based education to the students of Electronics and communication engineering and enable them to meet the ever changing needs of the society.

Mission of the Department

- ❖ *To emphasize on student focus learning environment.*
- ❖ *To prepare graduates who surpass in the engineering career, qualified to acquire technical knowledge and ethical values.*
- ❖ *To provide a platform to inculcate team spirit and leadership qualities to succeed in professional career.*

Program Specific Outcomes (PSOs)

- ❖ *PSO1: Analyze, simulate and design electronic and communication circuits for given specifications.*
- ❖ *PSO2: Design and test processor based systems*

Program Educational Objectives (PEOs)

- ❖ *PEO1: To create graduates with a strong base in Electronics & Communication Engineering.*
- ❖ *PEO2: To produce technically competent graduates with the ability to understand, analyse & develop electronic systems.*
- ❖ *PEO3: Ability to work with professional ethics and as a team member to realize the goals of the projects.*
- ❖ *PEO4: Excellence in professional career by attaining higher education.*

Student's Participation



Mr. Vinay S. B. a 7th semester student from the EEE Department and IEEE Student Branch Chair, and Mr. Ayush G. Y. a 7th semester student from the E&CE Department and IEEE Student Branch Secretary, are attending the IEEE Day Celebration and Student Branch Office Bearers Conclave at Sai Vidya Institute of Technology, Bengaluru, on 15th October 2025.

A Technical Talk On “VLSI and Embedded Systems” -17-10-2025



A Technical Talk on “VLSI and Embedded Systems” was organized by the **Department of Electronics and Communication Engineering** under the Forum ENCORE for the **3rd semester students** on **17th October 2025** at the **EEE Seminar Hall**. **Mr. Jayachandra Aradhya M. S., CEO of Silicon Microsystems**, was invited as the **guest speaker** for the session. The event was coordinated by **Prof. Sharanabasaveshwara H. B., Assistant Professor, Dept. of E&CE**, and **Prof. Kshitija.T, Assistant Professor, Dept. of E&CE**. **Mr. Jayachandra Aradhya M. S.**, a renowned professional with vast experience in the field of **Embedded Systems and VLSI Design**, addressed the students on the **importance of electronics**, explaining **how electronics is integrated into all modern devices** and discussing **FPGA designs** in detail.

