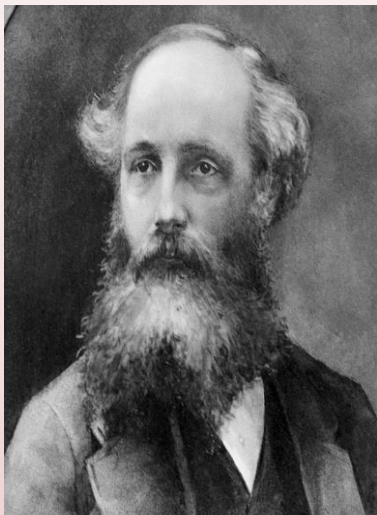


Newsletter

NOVEMBER
2025

"Action at a distance is not a fundamental principle of nature."



"In science, the most important thing is to modify and change one's ideas as science advances."

James Clerk
Maxwell



ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davangere

(A Unit of Jain Group of Institutions, Bangalore)



*Department of Electronics and
Communication Engineering*



Electro Infinity

Create, Enhance and Sustain



About the Department



Dr. Santosh Herur
Professor & Head
Dept. of E&CE
JIT, Davangere

Department of Electronics and Communication was started in the year 2011-12. The department emphasizes on imparting advanced technical knowledge in various fields like signal processing, VLSI Technology, Embedded system, wireless technology, microwave engineering, optical communication, and microelectronics and control systems. Highly qualified, experienced and dedicated staff members are the backbone of the department. Well equipped, sophisticated, precision and state of the art laboratories are centre of attraction in the campus. Another major strength of the department is its extensive collection of technical books, periodical and journals, ENCORE association of the department of E & CE aims at targeting innovations in the field of advanced communication and networking.

The Department has MOU with Karunadu Technologies Pvt. Ltd, Bengaluru, Abiyantrix, Soft Solutions, Davangere, Sai Tech, Mysore and PSN Hub IT consultancy services. The MOU organizations provide Internship training and conduct workshops on recent trends and organize seminars for the students to enhance their industrial exposure.

Editor's desk



Ms NAYANA T

*Assistant Professor
Dept. of E&CE,
JIT, Davanagere*

*Warm greetings to the readers of **Electro Infinity**!!*

We are pleased to share with you the November issue of the newsletter for the year 2025 with a Heartfelt wish of Kannada Rajyotsava and Kanakadas Jayanthi. We express our considerable gratitude and appreciation to everyone who have contributed to this Newsletter magazine. These contributions have required a generous amount of time and effort. It is this willingness to share knowledge, concerns and special insights with fellow beings that has made this magazine possible.

Finally we would like to conclude with these words The study of electromagnetism has led to the development of ideas which have transformed science- James Clerk Maxwell.

Happy reading

Thank you

NAYANA T

Vision of the Department

Endeavours to achieve academic excellence by providing value based education to the students of Electronics and communication engineering and enable them to meet the ever changing needs of the society.

Mission of the Department

- ❖ *To emphasize on student focus learning environment.*
- ❖ *To prepare graduates who surpass in the engineering career, qualified to acquire technical knowledge and ethical values.*
- ❖ *To provide a platform to inculcate team spirit and leadership qualities to succeed in professional career.*

Program Specific Outcomes (PSOs)

- ❖ *PSO1: Analyze, simulate and design electronic and communication circuits for given specifications.*
- ❖ *PSO2: Design and test processor based systems*

Program Educational Objectives (PEOs)

- ❖ *PEO1: To create graduates with a strong base in Electronics & Communication Engineering.*
- ❖ *PEO2: To produce technically competent graduates with the ability to understand, analyse & develop electronic systems.*
- ❖ *PEO3: Ability to work with professional ethics and as a team member to realize the goals of the projects.*
- ❖ *PEO4: Excellence in professional career by attaining higher education.*



ARKA Educational & Cultural Trust (R)

JGI JAIN INSTITUTE OF TECHNOLOGY

(A UNIT OF JAIN GROUP OF INSTITUTIONS, BENGALURU)



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

INVITES FOR THE CONTINUATION OF

“FOUR DAYS SKILL DEVELOPMENT PROGRAM”
on

EMBEDDED SYSTEM FOR PROJECT / PRODUCT DEVELOPMENT

RESOURCE PERSONS

Mr. Shivaraj V S
Manager
Abeyaantrix Edusoft LLP

Mr. Rakesh K B
Embedded Engineer
Abeyaantrix Edusoft LLP

Mrs. Deepthi G B
Mr. Sharanabasaveshwara HB
Coordinators

Dr. Santosh Herur
Convener

Dr. K B Prakash
Dean Academics

Mr. Praveen Anaji
Skill Lab Coordinator

Targetted Audience : 5th Sem Students



04 Nov 2025
9.30 AM



ECE Dept
JIT

Dr. Manjappa Sarathi
Advisor

Dr. Ganesh D B
Principal & Director

Four Days Skill Development Program on "Embedded system for Project/product development"

ARKA Educational & Cultural Trust (R)

JGI JAIN INSTITUTE OF TECHNOLOGY
(A UNIT OF JAIN GROUP OF INSTITUTIONS, BENGALURU)

NAAC GRADE A⁺ ACCREDITED INSTITUTIONS

NBA

IEEE BANGALORE SECTION

ISO 9001:2015

IQAAC

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

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Mrs. Deepthi G B Mr. Sharanabasaveshwara H B Coordinators	Dr. Santosh Herur Convener
Dr. K B Prakash Dean Academics	Mr. Praveen Anaji Skill Lab Coordinator

Targetted Audience : 5th Sem Students

04 Nov 2025
9.30 AM

ECE Dept
JIT

Dr. Manjappa Sarathi
Advisor

Dr. Ganesh D B
Principal & Director



A Four-Day Skill Development Program (SDP) on **"Embedded System for Project/Product Development"** was organized by the Department of E&CE in association with **Abeyaantrix Softlabs, Davanagere** for the 5th semester 'A' section students from **04th to 05th November 2025** at 09:30 AM in classroom **FF123**.

The Inauguration of SDP was organized at the Department of E&CE. **Mr. Shivaraj V. S., Manager, Abeyaantrix Softlabs**, and **Mr. Rakesh K. B., Embedded Engineer, Abeyaantrix Softlabs**, were invited as the resource persons for the program. **Dr. Santosh Herur, Convener, Professor & Head, Dept. of E&CE**, presided over the event. The program was coordinated by **Mrs. Deepthi G. B., Assistant Professor, Dept. of E&CE** and **Mr. Sharanabasaveshwara H. B., Assistant Professor, Dept. of E&CE**. During the inauguration, **Dr. Santosh Herur** addressed the students about the importance of the workshop.

On the same day, a session on Arduino followed by a hands-on programming session was conducted. The students actively participated and gained practical exposure to programming, which helped them strengthen their fundamentals and boosted their confidence in applying embedded system concepts to real-time projects.

Four Days Skill Development Program on "Embedded system for Project/product development"

A Four-Day Skill Development Program (SDP) on “Embedded System for Project/Product Development” was organized by the Department of E&CE in association with **Abeyaantrix Softlabs, Davangere** for the 5th semester ‘A’ section students from **04th to 05th November 2025** at 09:30 AM in classroom **FF123**.



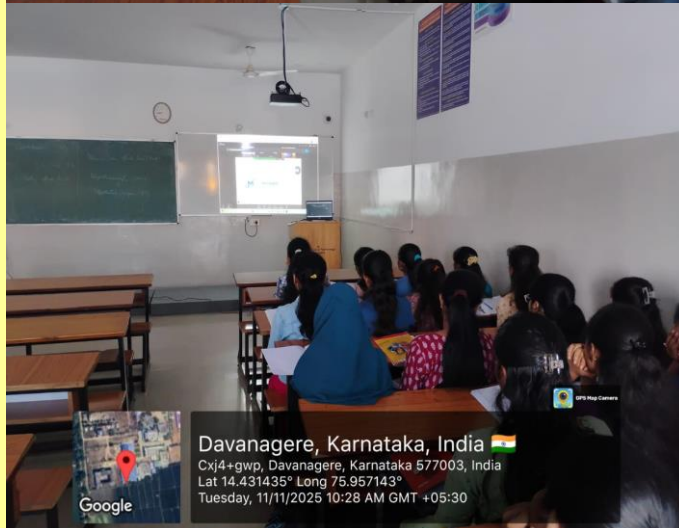
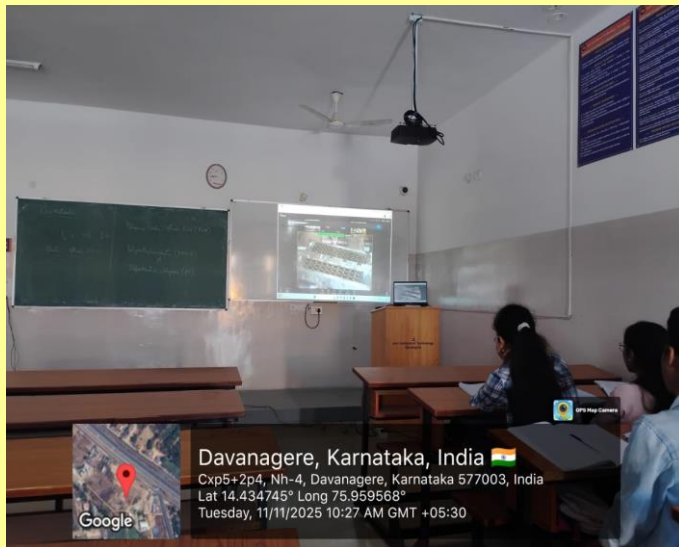
On the second day of the Skill Development Program, the resource person delivered a hands-on training session on interfacing LCD displays, analog and digital sensors, motors, and servo motors with the ESP32 microcontroller.

Industry Visit



As part of the *Innovation and Design Thinking* course for First Year ECE students, we visited **Maharaja Soap Industry** on **10th November 2025**, a leading manufacturer of soap products. The visit was coordinated by **Mr. Sharanabasaveshwara H. B., Assistant Professor, Dept. of ECE**. We were warmly welcomed by the **Production Manager**, who briefed us about the company's history, mission, and range of products. The factory tour began at the **raw material storage area**, where we observed various ingredients such as oils, fats, and alkalies used in soap production. The **soap-making process** involves several stages, including **saponification**, **mixing**, and **molding**. We observed how the ingredients are mixed and heated to produce soap noodles, which are later formed into bars. The **Quality Control team** explained the testing procedures that ensure the soap meets the required standards. We also visited the **packaging and labeling section**, where the finished products are packed and prepared for distribution. The visit concluded with an interactive **Q&A session**, during which we clarified our doubts and gained valuable insights into the challenges and opportunities in the soap manufacturing industry. Overall, the industrial visit was highly informative and provided us with practical knowledge about soap manufacturing processes. It was indeed a great learning experience!

Seminar on VLSI Job Roles and Skills Highlights Industry Trends and Future Prospects



A seminar on “**VLSI Job Opportunities**” was conducted for the **5th semester ECE students** on **11th November 2025** to provide insights into various career paths within the VLSI industry. The session was delivered by **Mr. Shiva Sai K, Physical Design Engineer** at **Takshila VLSI Technologies**, and coordinated by **Mr. Sharanabasaveshwara H. B, Assistant Professor**, Department of ECE, JITD. The seminar highlighted several key job roles such as **Design Engineer, Verification Engineer, Physical Design Engineer, DFT Engineer, and Validation Engineer**, explaining their responsibilities in the VLSI design flow. Mr. Shiva Sai emphasized the essential skills required to build a successful career in this domain, including proficiency in **HDLs (Verilog, VHDL)**, a strong understanding of **digital and analog circuit design**, familiarity with **EDA tools** like Cadence, Synopsys, and Mentor Graphics, and knowledge of **semiconductor manufacturing processes**. He also discussed current **industry trends**, such as the increasing demand for VLSI professionals across sectors like **automotive, aerospace, and IoT**, along with the growing influence of **AI and machine learning** in chip design. The seminar concluded by outlining the excellent **career prospects** in the field, including opportunities in leading semiconductor companies and start-ups, with strong potential for **growth and professional development**.

Faculty Achievement

ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ

ಎಂ ಟಿ ಯು ಅಧಿನಿಯಮ ೧೯೯೪ ರ ಅಡಿಯಲ್ಲಿ ಕರ್ನಾಟಕ ಸರ್ಕಾರದಿಂದ ಸ್ಥಾಪಿತವಾದ ರಾಜ್ಯ ವಿಶ್ವವಿದ್ಯಾಲಯ

Visvesvaraya Technological University

(State University of Government of Karnataka Established as per the VTU Act, 1994)

"Jnana Sangama" Belagavi-590018, Karnataka, India

Dr. Ujwal U. J. M.Tech., Ph.D.

Registrar (Evaluation)

Ref. No./VTU/Exam/BOE/2025-26/1099

Phone: (0831) 2498131

Date : 13 NOV 2025

Revised

EC/TE/EI/BM/UE/VL (COMPOSITE) BOARD

Sl. No.	Name of the Chairman/Members	College	C / M
1	Dr. Radha B L	Bangaluru Institute of Technology, Bangaluru (1BI)	Chairman
2	Dr. Manju Devi	Oxford College of Engineering Bangaluru (1OX)	Member
3	Dr. Shrikanth Rao K	Vivekananda College of Engg & Tech, Puttur (4VP)	Member
4	Dr. S G Shivaprasad Yadav	M.S Ramaiah Institute of Tech., Bangaluru (1MS)	Member
5	Dr. Keshavamurthy	Atria Institute of Technology, Bangaluru (1AT)	Member
6	Dr. Hareesh Kumar	R V Inst. of Tech. & Mgmt, Bengaluru (1RF)	Member
7	Dr. Bhagya R	Rashtrereya Vidyalaya College of Engg, Bangaluru (1RV)	Member
8	Dr. M Nagabhushan	M.S Ramaiah Institute of Tech., Bangaluru (1MS)	Member
9	Dr. Yogesh G S	East Point College of Eng. & Tech, Bangaluru (1EP)	Member
10	Dr. Venkatesha M	Sai Vidya Inst of Tech, Bangaluru (1VA)	Member
11	Dr. Ravi. J	Global Academy of Technology, Bangaluru (1GA)	Member
12	Dr. Aswathappa P	Bangaluru Institute of Technology, Bangaluru (1BI)	Member
13	Prof. Sowmya M S	J.S.S. Academy of Technical Education Bangaluru (1JS)	Member
14	Dr. M D Nandeesh	M.S Ramaiah Institute of Tech., Bangaluru (1MS)	Member
15	Dr. Madhusudhan K N	B.M.S. College of Engineering, Bangaluru (1BM)	Member
16	Dr. Raviraj Havaladar	K.L.E. College of Engg & Tech, Belagavi (2KL)	Member
17	Dr. Somashekhar G C	Akshaya Institute of Technology, Tumkuru (1AK)	Member
18	Dr. Jijesh J J	Sri,Venkateswara College of Engg. Bangaluru (1VE)	Member
19	Dr. Dankan Gowda V	B.M.S Inst. of Tech. and Mgmt, Bangaluru (1BY)	Member
20	Dr. Bhuvaneshwari P	ACS College of Engineering, Bangaluru (1AH)	Member
21	Prof. Suraj Kadli	K.L.S. VDRIT, Haliyal (2VD)	Member
22	Dr. Aravind Jadhav	Angadi Inst of Technology & Magt, Belagavi (2AG)	Member
23	Dr. Mahesh B Neelagar	Dept of PG Studies, VTU, Belagavi (2VX)	Member
24	Dr. Rajashekhar U	Government Engg. College, Huvina Hadagali (2GB)	Member
25	Dr. Chinna V Goudar	RYMEC, Bellari (3VC)	Member
26	Dr. G C Manjunath	Proudadevaraya Institute of Tech, Hospet (3PG)	Member
27	Dr. N Satisha	Government Engg. College, Kriahnarajapate (4GK)	Member
28	Dr. Santhosh Kumar D R	U.B.D T. College of Engineering, Davangere (4UB)	Member
29	Prof. Rajani Rai	Vivekananda College of Engg Puttur (DK) (4VP)	Member
30	Dr. Harisha S B	Jawaharlal Nehru National College of Engg.(4JN)	Member
31	Dr. Rajendra Soloni	Jain Institute of Technology, Davanagere (4JD)	Member
32	Dr. Naveen Kumar H N	Vidya Vardhaka College of Engineering, Mysuru (4VV)	Member
33	Dr. Neelappa	Government Engineering College, Hassan (4GH)	Member
34	Dr. Praveen Chitti	Maratha Mandals Engg. College, Belagavi (2MM)	Member
35	Dr. Jalaja S	Bengaluru Institute of Technology, Bangaluru (1BI)	Member
36	Dr. Pratibha MK	Academy for Technical & Magt, Mysore (4AD)	Member

Dr. Rajendra Soloni, Associate Professor, Department of Electronics and Communication Engineering, has been appointed as a Member of the Board of Examiners at VTU, Belagavi, for the Academic Year 2025–2026.

Hearty congratulations to Dr. Rajendra Soloni on this accomplishment. The College Management, Principal, Head of the Department, and all the staff members of the E&CE Department extend their best wishes.

Best Faculty Chapter Award for the year 2025 bagged by JITD

Jain Institute of Technology, Davanagere, has been awarded the “Best Faculty Chapter of ISTE – Karnataka Section” for its outstanding activities carried out during the year 2025. The award will be conferred during the inaugural function of the **55th ISTE National Annual Faculty Convention**, scheduled to be held at **Sri Manakula Vinayagar Engineering College, Puducherry**, from **29th to 30th November 2025**. This achievement was made possible through the initiative and guidance of **Dr. Ganesh D. B., Principal and Director, JITD**, and **Dr. Manjappa Sarathi, Advisor, JITD**. Hearty congratulations to all the **faculty members and ISTE coordinators** on this remarkable accomplishment. The **College Management, Principal, HOD**, and all faculty members extend their warm wishes and appreciation for this well-deserved recognition.

A Session on 'My Story / Motivational Expert Sessions by Successful Innovators & Entrepreneurs

JGI Jain Institute of Technology, Davanagere
(A Unit of Jain Group of Institutions, Bengaluru)

JITD INSTITUTION'S INNOVATION COUNCIL
in association with
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
ORGANIZES

A Session on My Story / Motivational Expert Sessions by Successful Innovators & Entrepreneurs

Resource Person
Gautham Nayak
Co-Founder & COO
InUnity LLP

Mrs. Anusha T S
Mrs. Mounashree B
Faculty IIC Coordinators

Dr. Santosh Herur
Professor and HOD, ECE
Director, IQAC
IIC Vice President, JITD

Dr. Sarathi Manjappa
Advisor, JITD

Mr. Harish K S
IIC Convener

Dr. Ganesh D B
Principal & Director, JITD

26 November 2025 **11:00AM TO 1:00PM**
Venue: JITD Auditorium



JITD Institution's Innovation Council (IIC), in association with the Department of Electronics and Communication Engineering, organized a session on **"My Story / Motivational Expert Sessions by Successful Innovators & Entrepreneurs"** on **26th November 2025** at the JITD Auditorium. **Mr. Gautham Nayak, Co-Founder & COO of InUnity LLP**, was invited as the Resource Person for the program. **Dr. Ganesh D. B., Principal and Director**, and **Dr. Sarathi Manjappa, Advisor, JITD**, conveyed their best wishes for the event. **Dr. Santosh Herur, Professor and Head, Department of ECE, IQAC Director, and IIC Vice President**, addressed the gathering and motivated the students by sharing insights about successful innovators. **Prof. Harish K. S., Convener, Prof. Anusha T. S., IIC Coordinator, and Prof. Mounashree B., IIC Co-Coordinator, Department of ECE**, were also present during the program. A total of **150 first-semester students** from various departments attended the session. All the teaching and non-teaching staff members of the Institute were also present for the event.

Faculty Enhance Professional Outreach at IIC Regional Meet



The faculty members of the Department of Electronics and Communication Engineering attended the **IIC Regional Meet** organized by the **Ministry of Education's Innovation Cell** and the **All-India Council for Technical Education (AICTE)**. The event was held at Bapuji Institute of Engineering and Technology, Davanagere, on 28th November 2025. Dr. Santosh Herur, Professor and Head of the Department of E&CE, Prof. Savitha Mamadapur, Assistant Professor, Prof. Anusha T. S., Assistant Professor, Prof. Nayana T., Assistant Professor, Prof. Ayesha Siddiq Khanum A., Assistant Professor, and Prof. Mounashree B., Assistant Professor, were among the faculty members who attended the IIC Regional Meet.

Best Faculty Chapter Award for the year 2025 bagged by JITD



Jain Institute of Technology, Davanagere, has been awarded the **“Best Faculty Chapter of ISTE – Karnataka Section”** for its outstanding activities during the year 2025. On behalf of the institution, Prof. Savitra C. T. and Prof. Anusha T. S., Assistant Professors from the Department of E&CE, received the award at the 55th ISTE National Annual Faculty Convention held at Sri Manakula Vinayagar Engineering College, Puducherry, on 29th November 2025. The College Management, Principal, HOD, and all faculty members extend their heartfelt congratulations and appreciation to the ISTE coordinators and faculty members for this well-deserved achievement.