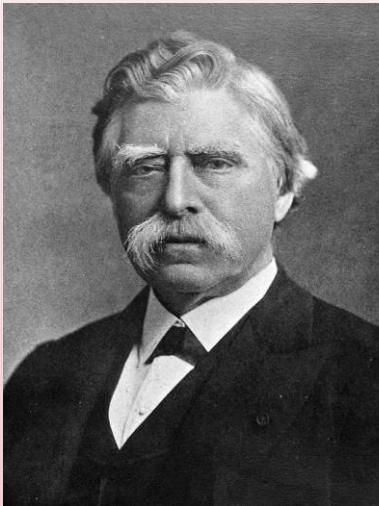


Newsletter

MAY
2025

"I can only say that it is a great honour for me to be a pioneer of a system that has given a great gift to humanity."



"In the new era, thought itself will be transmitted by wireless."

David Edward
Hughes



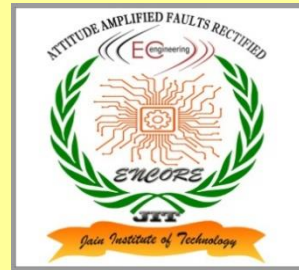
ARKA Educational & Cultural Trust (Regd.)

Jain Institute of Technology, Davangere

(A Unit of Jain Group of Institutions, Bangalore)



*Department of Electronics and
Communication Engineering*



Electro Infinity

Create, Enhance and Sustain



About the Department



Dr. Santosh Herur
Professor & Head
Dept. of E&CE
JIT, Davangere

Department of Electronics and Communication was started in the year 2011-12. The department emphasizes on imparting advanced technical knowledge in various fields like signal processing, VLSI Technology, Embedded system, wireless technology, microwave engineering, optical communication, and microelectronics and control systems. Highly qualified, experienced and dedicated staff members are the backbone of the department. Well equipped, sophisticated, precision and state of the art laboratories are centre of attraction in the campus. Another major strength of the department is its extensive collection of technical books, periodical and journals, ENCORE association of the department of E & CE aims at targeting innovations in the field of advanced communication and networking.

The Department has MOU with Karunadu Technologies Pvt. Ltd, Bengaluru, Abiyantrix, Soft Solutions, Davangere, Sai Tech, Mysore and PSN Hub IT consultancy services. The MOU organizations provide Internship training and conduct workshops on recent trends and organize seminars for the students to enhance their industrial exposure.

Editor's desk



Ms NAYANA T

*Assistant Professor
Dept. of E&CE,
IIT, Davanagere*

*Warm greetings to the readers of **Electro Infinity**!!*

We are pleased to share with you the May issue of the newsletter for the year 2025 with a Heartfelt wish of International Labours Day. We express our considerable gratitude and appreciation to everyone who have contributed to this Newsletter magazine. These contributions have required a generous amount of time and effort. It is this willingness to share knowledge, concerns and special insights with fellow beings that has made this magazine possible.

***Finally we would like to conclude with these words** The sounds produced by insects crawling over the instrument could be distinctly heard. – David Edward Hughes.*

Happy reading

Thank you

NAYANA T

Vision of the Department

Endeavours to achieve academic excellence by providing value based education to the students of Electronics and communication engineering and enable them to meet the ever changing needs of the society.

Mission of the Department

- ❖ *To emphasize on student focus learning environment.*
- ❖ *To prepare graduates who surpass in the engineering career, qualified to acquire technical knowledge and ethical values.*
- ❖ *To provide a platform to inculcate team spirit and leadership qualities to succeed in professional career.*

Program Specific Outcomes (PSOs)

- ❖ *PSO1: Analyze, simulate and design electronic and communication circuits for given specifications.*
- ❖ *PSO2: Design and test processor based systems*

Program Educational Objectives (PEOs)

- ❖ *PEO1: To create graduates with a strong base in Electronics & Communication Engineering.*
- ❖ *PEO2: To produce technically competent graduates with the ability to understand, analyse & develop electronic systems.*
- ❖ *PEO3: Ability to work with professional ethics and as a team member to realize the goals of the projects.*
- ❖ *PEO4: Excellence in professional career by attaining higher education.*



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A Technical Talk On

VOICE OVER IP

The Technology behind Modern Communication

Organized by

Department of Electronics & Communication Engineering



1. Ravindra G Palankar

Senior Technical Specialist
Avaya Technologies

2. Kavya Jain A

Engineer - Program Management
Avalon Technologies Ltd.



Coordinators

1. Dr. Ravikumar K. I

2. Prof. Nayana T



SATURDAY

03 MAY, 2025



CSE SEMINAR HALL



09.00 AM

*CS&E,E&CE,EEE, validity upto 30/06/2026

Dr. Santosh Herur

Professor & Head
Dept. of ECE, JIT, Davangere

Dr. Manjappa S

Advisor
JIT, Davangere

Dr. Ganesh D B

Principal
JIT, Davangere

A Technical Talk On Voice Over IP -- 03/05/2025

JGI JAIN INSTITUTE OF TECHNOLOGY
(A Unit of Jain Group of Institutions, Bengaluru)
323, Near Veereshwara Punyashrama, Bada cross, Davangere- 577003
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A Technical Talk On
VOICE OVER IP
The Technology behind Modern Communication

Organized by
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1. Ravindra G Palankar
Senior Technical Specialist
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Engineer - Program Management
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Coordinators
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2. Prof. Nayana T

SATURDAY
03 MAY, 2025

CSE SEMINAR HALL
09.00 AM

*CS&E&CEEEE, validity upto 30/06/2026

Dr. Santosh Herur
Professor & Head
Dept. of ECE, JIT, Davangere

Dr. Manjappa S
Advisor
JIT, Davangere

Dr. Ganesh D B
Principal
JIT, Davangere



Images of technical talk

A Technical talk on “Voice Over IP – The Technology Behind Modern Communication” was organized by the Department of Electronics & Communication Engineering for 6th semester students on 3rd May 2025 at the CSE Seminar Hall. Alumni of JITD **Mr. Ravindra G. Palankar**, Senior Technical Specialist at Avaya Technologies, and **Ms. Kavya Jain A**, Engineer – Program Management at Avalon Technologies Ltd. Were invited as guest speakers. **Dr. Santosh Herur**, Professor and Head, Dept. of E&CE graced the event with his presence. **Dr. Ravikumar K. I.**, Associate Professor, Dept. of E&CE and **Prof. Nayana T**, Assistant Professor, Dept of E&CE coordinated the event.

Mr. Ravindra G. Palankar in his session on voice over IP gave insights into the fundamentals of IP, voice communication over the internet and computer networks, and discussed industry applications and career opportunities in the field. **Ms. Kavya Jain A** addressed the students on electronic project development and marketing skills, and emphasized the importance of utilizing project development methodologies for effective project planning, organization, and execution. The event proved to be highly informative and beneficial for the students, offering a blend of technical knowledge and practical industry exposure.

Faculty Achievement



The Department of Electronics and Communication Engineering proudly congratulates **Prof. Ayesha Siddiq Khanum M** on successfully completing the prestigious **NPTEL course on Quantum Algorithms and Cryptography** with a commendable score of 75%, earning her the **Elite + Silver** certification. This remarkable achievement showcases her deep understanding of emerging technologies in quantum computing and cryptography. It stands as a testament to her unwavering dedication to academic growth, continuous professional development, and pursuit of excellence. Her success not only enriches her own knowledge but also adds immense value to the department's academic and research endeavors in advanced technological domains.



Elite

NPTEL ONLINE CERTIFICATION
(Funded by the MoE, Govt. of India)

This certificate is awarded to
AYESHA SIDDIQA KHANUM M
for successfully completing the course
Quantum Algorithms and Cryptography
with a consolidated score of **75** %

Online Assignments	21.5/25	Proctored Exam	53.25/75
--------------------	---------	----------------	----------

Total number of candidates certified in this course: **240**


Prof. Andrew Thangaraj
Chair
Centre for Outreach and Digital Education, IITM

Jan-Apr 2025
(12 week course)


Prof. Vignesh Muthuvijayan
NPTEL Coordinator
IIT Madras

 Indian Institute of Technology Madras



Roll No: NPTEL25CS61S643900104

To verify the certificate 

No. of credits recommended: 3 or 4

Faculty Achievement



Sl. No.	VGST Ref No.	Details of Principal Investigator	Title of the Project	Scheme	Subject	Funds to be released during the 1st year (Rs. in lakhs)	Funds to be released during the 2nd year (Rs. in lakhs)	Total Amount (Rs. in lakhs)
5	VRN/000059/23-24	Dr. Babu S Chavhan Assistant Professor, Department of Civil Engineering, Government Engineering College, Davanagi, Haveri - 581 110	Integrated Scientific Framework for the Study and Preservation of Historical Architectural Forms in India	GRE	Civil Engineering	25.00	15.00	40.00
	VRN/004651/23-24	Dr. Arjun U Associate Professor and HOD, Department of Computer Science and Engineering, JES Institute of Technology & Management, Bijlur	Centre of Excellence in Blockchain- to create a Secure and Decentralized Technology	GRE	Computer Science/Information Science	25.00	15.00	
7	VRN/000150/23-24	Dr. Santosh Herur Professor & Head, Department of Electronics & Communication Engineering, Jain Institute of Technology, 323, Bida Cross, Near Blind School, Awaragere, Davanagere - 577 005	Development of a Universal FPGA-Based Embedded Development Board for Multi-Sector Applications Utilizing RISC-V Architecture	GRE	Electronics and Communication Engineering/ Telecommunication Engineering	25.00	15.00	40.00
	VRN/000152/23-24	Dr. Jyoti Bhat Associate Professor, Department of Civil Engineering, Mangal Institute of Technology, Udupi - Karkala Road, Karkala Nagar, Mangal - 575 104	Groundwater Dynamics in Udupi District: An isotopic based approach	GRE	Geology, Earth & Environmental Sciences	25.00	15.00	40.00
9	VRN/008153/23-24	Dr. Jaya Christy K G Assistant Professor, Department of Mechanical Engineering, M. S. Ramaiah Institute of Technology, Vidya Southa, MSR Nagar, MSRIT Post, Bengaluru - 560 054	Development of Sustainable Wire arc Additive manufacturing for refurbishing aerospace components	GRE	Mechanical Engineering	25.00	15.00	40.00
10	VRN/003802/23-24	Dr. Naganja Professor, Department of Applied Zoology, Kuvempu University, Jyamasahadri, Shankaragatta - 577 451, Bhadravati Taluk, Shivamogga District	Molecular Genetic study of Sidi Tribal populations	GRE	Medical & Life Sciences	25.00	15.00	40.00
11	VRN/008545/23-24	Dr. Girish B.C Associate Professor & Head, Department of Veterinary Pathology, Veterinary College, Gokula, Vidyanagar, Hassan - 573 202	Comparative transcriptomic analysis of cutaneous squamous cell carcinomas	GRE	Medical & Life Sciences	25.00	15.00	40.00

Page 2 of 3

Prof. Navakanta Bhat
Chairman, VGST Sub-Committee
IISc, Bengaluru - 560 012

Prof. C.N.R. Rao, F.R.S.
Chairman
Visvesvaraya National Institute of Technology, Kharagpur, Bengaluru

The Jain Institute of Technology, Davanagere, has been awarded a prestigious ₹40 lakh grant by the VGST, Government of Karnataka under Grants for Research Excellence scheme for its project titled “Development of a Universal FPGA-Based Embedded Development Board for Multi-Sector Applications Utilizing RISC-V Architecture.” This innovative initiative, led by Dr. Santosh Herur (Principal Investigator), Dr. Santosh M. Nejekar (Co-Principal Investigator), and coordinated by Mr. Sharanabasaveshwara H. B., aims to develop an open-source, reconfigurable platform using RISC-V and FPGA technologies. Designed to support affordable experimentation, prototyping, and deployment, the development board will serve diverse sectors such as healthcare, automotive, and IoT, while advancing self-reliant technological innovation in line with the Make-in-India initiative. Hosted at Jain Institute of Technology in collaboration with Silicon Microsystems, Bengaluru, the project marks a significant milestone in fostering cutting-edge research. The Management, Dr. Ganesh D. B, Principal and Director, Advisor Dr. Manjappa Sarathi, faculty, staff, and students have extended heartfelt congratulations to the entire project team for their commitment and efforts in securing this grant and enhancing the Institute’s research and innovation ecosystem.



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A Two Days Skill Development Program On

Microcontroller 8051 Interfacing Applications using Proteus Tool

Organized by

Department of Electronics & Communication Engineering

In Association with

Abeyaantrix Softlabs

Target Audience : 4th Semester 'A' Section E&CE Students

Resource Persons

1. Mr. Shivaraj V S

Manager, Abeyaantrix Softlabs

2. Mr. Rakesh K B

Embedded Engineer
Abeyaantrix Softlabs

Coordinators

1. Prof. Anusha K

2. Prof. Nayana T



13th & 14th May 2025

09.30 AM - 04.30 PM



ECE Class Room : FF125

*CS&E,E&CE,EEE, validity upto 30/06/2026

Dr. Santosh Herur

Convener

Professor & Head, Dept. of ECE

Dr. Manjappa S

Advisor

JIT, Davangere

Dr. Ganesh D B

Principal & Director

JIT, Davangere

Two Days Skill Development Program on "Microcontroller 8051 interfacing applications using Proteus Tool"

JGI ARKA EDUCATIONAL & CULTURAL TRUST (REGD.)
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A Two Days Skill Development Program On
Microcontroller 8051 Interfacing Applications
using Proteus Tool

Organized by
Department of Electronics & Communication Engineering
In Association with
Abeyaantrix Softlabs

Target Audience : 4th Semester 'A' Section E&CE Students

Resource Persons
1. Mr. Shivaraj V S
Manager, Abeyaantrix Softlabs
2. Mr. Rakesh K B
Embedded Engineer
Abeyaantrix Softlabs

Coordinators
1. Prof. Anusha K
2. Prof. Nayana T

13th & 14th May 2025
09.30 AM - 04.30 PM
ECE Class Room : FF125

Dr. Santosh Herur **Dr. Manjappa S** **Dr. Ganesh D B**
Convener Advisor Principal & Director
Professor & Head, Dept. of ECE JIT, Davangere JIT, Davangere

*CS&E,E&CE,EEE, validity upto 30/06/2026



Images of skill development program

A Two Days Skill Development Program (SDP) on "Microcontroller 8051 interfacing applications using Proteus Tool" was organized by Department of E&CE in association with Abeyaantrix softlabs, Davangere for the 4th semester 'A' section students from 13th to 14th May 2025 as a part of Course Activity.

The Inauguration of SDP was organized at the Department of E&CE. **Mr. Shivaraj V S**, Manager, Abeyaantrix Softlabs and **Mr. Rakesh K B**, Embedded Engineer, Abeyaantrix Softlabs were invited as the resource persons for the SDP. **Dr. Santosh Herur**, Convener, Professor & Head, Dept. of E&CE presided over the event. Prof. Anusha K and Prof. Nayana T coordinated the Event. Resource Person, Mr. Shivaraj V S addressed the gathering and briefed about the interfacing and proteus tool. Dr. Santosh Herur, Convener, Professor & Head, Dept. of ECE addressed the students and gave insights about the importance of interfacing, its applications and explained the versatility of Proteus tool and its uses in project Development and also gave his best wishes for the successful conduction of the event.

Day 1 : Session – 1

In Session 1, Resource Person, Mr. Rakesh K B explained basics of Microcontroller and its applications gave brief introduction to I/O Programming in Microcontroller 8051. He also explained C and Assembly programming for LCD and DAC interfacing, Concepts of Port Programming and Embedded C.



Day 1 : Session – 2

In Session 1, Resource Person, Mr. Rakesh K B explained the steps for Proteus tool installation and its operation. The session also covered Hands-on practice : LCD & DAC Programming using Proteus software.



Two Days Skill Development Program on “Microcontroller 8051 interfacing applications using Proteus Tool”

A Two Days Skill Development Program (SDP) on “**Microcontroller 8051 interfacing applications using Proteus Tool**” was organized by Department of E&CE in association with Abeyaantrix softlabs, Davangere for the **4th semester ‘A’ section students from 13th to 14th May 2025** as a part of Course Activity.

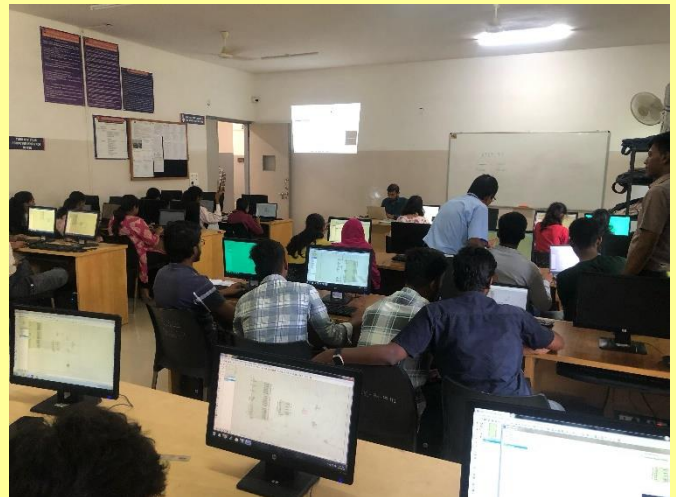
Day 2: Session – 3

In Session 3, Resource person, Mr. Rakesh K. B., explained ADC, Stepper motor, PWM, and DC motor programming. He also provided a functional overview and explained the interfacing techniques.



Day 2: Session – 4

In Session 4, Resource person, Mr. Rakesh K. B., conducted a hands-on practice session on ADC, stepper motor, PWM, and DC motor with practical implementation using the Proteus tool. The session also included Q&A, simulation testing, and a quiz.





ARKA EDUCATIONAL & CULTURAL TRUST (REGD.)

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A Two Days Skill Development Program On

Microcontroller 8051 Interfacing Applications using Proteus Tool

Organized by

Department of Electronics & Communication Engineering

In Association with

Abeyaantrix Softlabs

Target Audience : 4th Semester 'B' Section E&CE Students

Resource Persons

1. Mr. Shivaraj V S

Manager, Abeyaantrix Softlabs

2. Mr. Rakesh K B

Embedded Engineer
Abeyaantrix Softlabs

Coordinators

1. Prof. Anusha K

2. Prof. Nayana T



19th & 20th May 2025

09.30 AM - 04.30 PM



ECE Class Room : FF124

*CS&E,E&CE,EEE, validity upto 30/06/2026

Dr. Santosh Herur

Convener

Professor & Head, Dept. of ECE

Dr. Manjappa S

Advisor

JIT, Davangere

Dr. Ganesh D B

Principal & Director

JIT, Davangere

Two Days Skill Development Program on “Microcontroller 8051 interfacing applications using Proteus Tool”

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A Two Days Skill Development Program On
Microcontroller 8051 Interfacing Applications
using Proteus Tool

Organized by
Department of Electronics & Communication Engineering
In Association with
Abeyaantrix Softlabs

Target Audience : 4th Semester 'B' Section E&CE Students

Resource Persons

- 1. Mr. Shivaraj V S**
Manager, Abeyaantrix Softlabs
- 2. Mr. Rakesh K B**
Embedded Engineer
Abeyaantrix Softlabs

Coordinators

- 1. Prof. Anusha K**
- 2. Prof. Nayana T**

19th & 20th May 2025
09.30 AM - 04:30 PM
ECE Class Room : FF124

Dr. Santosh Herur Convener
Professor & Head, Dept. of ECE

Dr. Manjappa S Advisor
JIT, Davangere

Dr. Ganesh D B Principal & Director
JIT, Davangere

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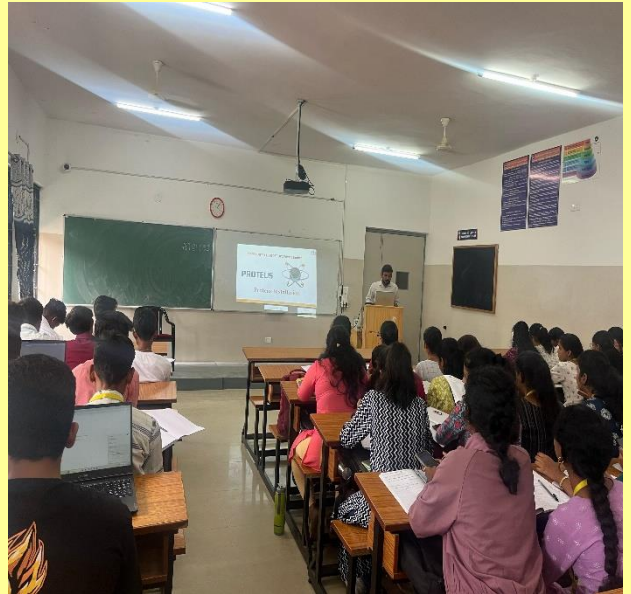
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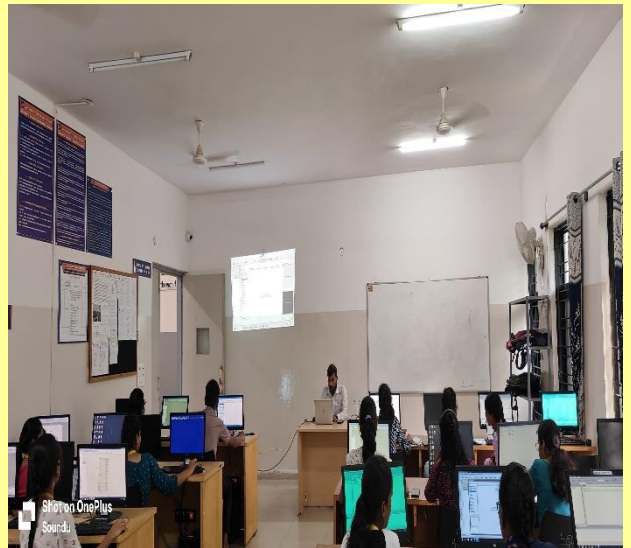
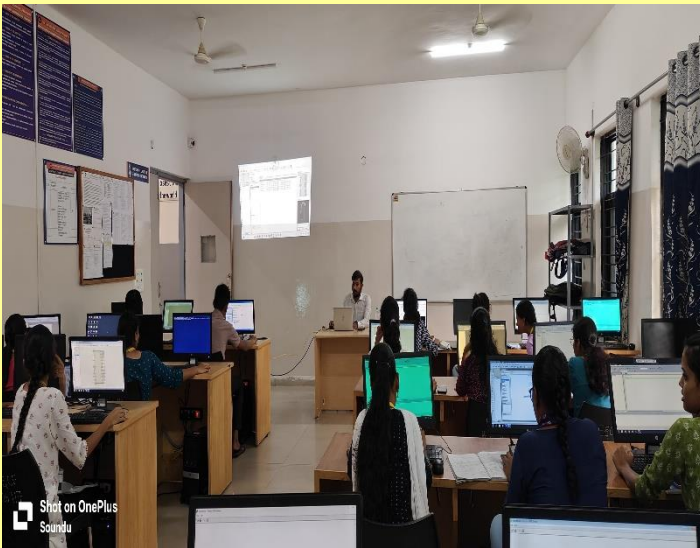
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Faculty Achievement



The Department of Electronics and Communication Engineering proudly congratulates **Prof. Ayesha Siddiqa Khanum M** on successfully completing the prestigious **NPTEL courses: *Programming, Data Structures and Algorithms Using Python and Problem Solving Through Programming in C***, with a commendable score of **57%**, earning her official certification. This achievement reflects her strong grasp of foundational and emerging concepts in programming and algorithmic problem-solving. It is a testament to her unwavering commitment to academic excellence, continuous professional development, and a passion for lifelong learning.



NPTEL ONLINE CERTIFICATION
(Funded by the MoE, Govt. of India)


Skill India
कौशल भारत - कुशल भारत

This certificate is awarded to
AYESHA SIDDIQA KHANUM M
for successfully completing the course

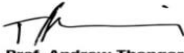


Programming, Data Structures And Algorithms Using Python

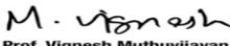
with a consolidated score of **57** %

Online Assignments	25/25	Proctored Exam	31.88/75
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Total number of candidates certified in this course: **1792**


Prof. Andrew Thangaraj
Chair
Centre for Outreach and Digital Education, IITM

Jan-Mar 2025
(8 week course)


Prof. Vignesh Muthuvijayan
NPTEL Coordinator
IIT Madras

 Indian Institute of Technology Madras



Roll No: NPTEL25CS59S336700044 To verify the certificate  No. of credits recommended: 2 or 3



NPTEL ONLINE CERTIFICATION
(Funded by the MoE, Govt. of India)


Skill India
कौशल भारत - कुशल भारत

This certificate is awarded to
AYESHA SIDDIQA KHANUM M
for successfully completing the course

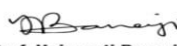


Problem Solving Through Programming In C

with a consolidated score of **57** %

Online Assignments	24.88/25	Proctored Exam	32.25/75
--------------------	----------	----------------	----------

Total number of candidates certified in this course: **2550**


Prof. Haimanti Banerji
Coordinator, NPTEL
IIT Kharagpur

Jan-Apr 2025
(12 week course)

 Indian Institute of Technology Kharagpur



Roll No: NPTEL25CS56S543900103 To verify the certificate  No. of credits recommended: 3 or 4

Two Days Skill Development Program on “Microcontroller 8051 interfacing applications using Proteus Tool”

A Two Days Skill Development Program (SDP) on “**Microcontroller 8051 interfacing applications using Proteus Tool**” was organized by Department of E&CE in association with Abeyaantrix softlabs, Davangere for the **4th semester ‘B’ section students from 19th to 20th May 2025** as a part of Course Activity.

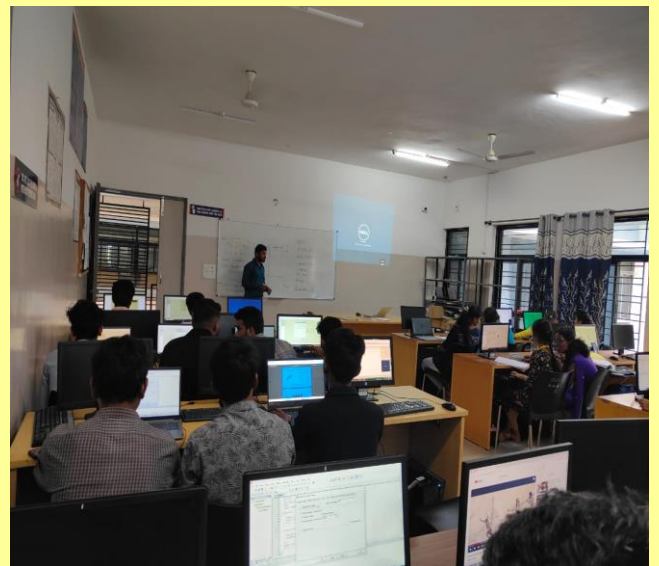
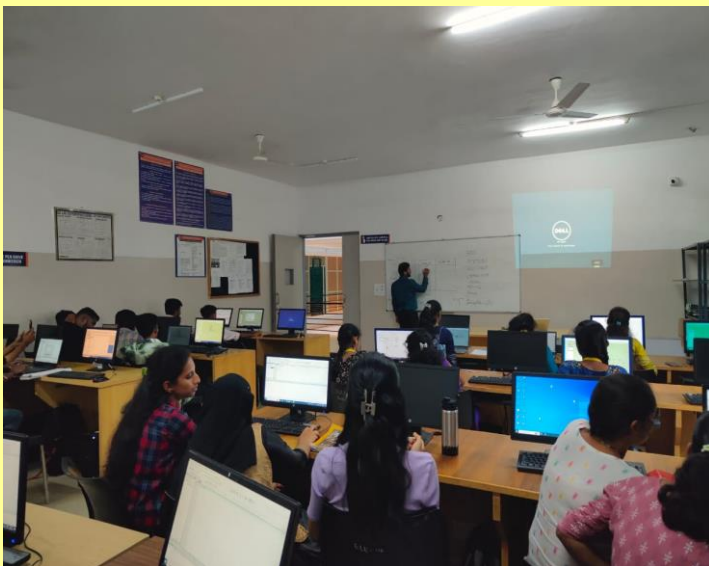
Day 2: Session – 3

In Session 3, Resource person, Mr. Rakesh K. B., explained ADC, Stepper motor, PWM, and DC motor programming. He also provided a functional overview and explained the interfacing techniques.

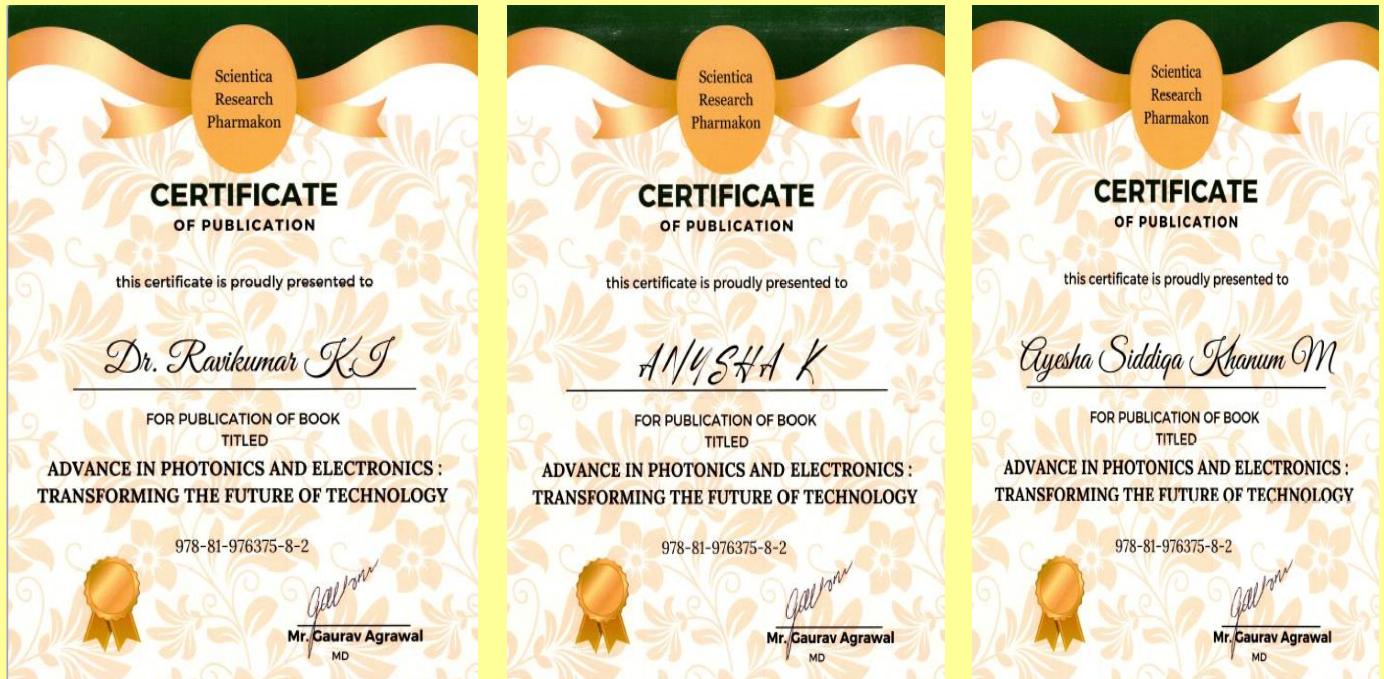


Day 2: Session – 4

In Session 4, Resource person, Mr. Rakesh K. B., conducted a hands-on practice session on ADC, stepper motor, PWM, and DC motor with practical implementation using the Proteus tool. The session also included Q&A, simulation testing, and a quiz.



Faculty Achievement



Dr. Ravikumar K. I, Associate Professor and **Mrs. Anusha K** and **Ms. Ayesha Siddiqua Khanum**, Assistant Professor, Dept. of E&CE have published a book titled "**Advances in Photonics and Electronics : Transforming the future of Technology**" in Scientica Research Pharmakon publications.

Hearty congratulations to the team on this accomplishment from College Management, Principal, HOD and all the staff members of E&CE Department.

Faculty Achievement



Electronics & ICT Academy
Indian Institute of Technology Guwahati
An Initiative of Ministry of Electronics & Information
Technology (MeitY), Government of India



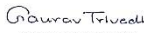
Ref No.: EICT/P-II/OF/24-25/004/078

Grade Certificate

Name of Participant : Mr. Sharanabasaveshwara H B
Father's Name : Bhuvaneshwara H
Mother's Name : Jayashree H B
Date of Birth : 29/02/1992
Course Name : Generative AI and DevOps
Duration : 17 – 22 February, 2025

Evaluation	Total Marks	Marks Obtained	Percentage
Theory	30	27	91%
Attendance	30	30	
Project	40	34	
Total	100	91	

Awarded Grade is "A"
(Refer overleaf for the grading formula)


Prof. Gaurav Trivedi
Principal Investigator

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Grading Formula

Percentage	Grades
Percentage > 80	Grade A
60 < Percentage ≤ 80	Grade B
Percentage ≤ 60	Grade C



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The Department of Electronics and Communication Engineering proudly congratulates **Prof. Sharanabasaveshwara H. B.** for successfully clearing the examination on **Generative AI and DevOps** conducted by **Indian Institute of Technology (IIT) Guwahati** as part of the **Faculty Development Program (FDP)** held at **BIET Davangere**. He has also been awarded a **Grade A Certificate** in recognition of his outstanding performance. This achievement reflects his dedication to continuous learning and excellence in emerging technologies.

Forum Valedictory and Farewell Ceremony



Images of forum valedictory and farewell

The Department of Electronics and Communication Engineering organized the valedictory function of its department forum, *Yaadein*, along with a farewell for the 2021–2025 batch students on 31st May 2025 at the JITD Auditorium. **Mr. Mallesh V. S.**, Technical Team Manager and Trainer at Abeyaantrix Softlab, Davangere, was invited as the Chief Guest. The event was presided over by **Dr. Manjappa S. Sarathi**, Advisor, JITD, **Dr. Ganesh D. B.**, Principal & Director, JITD, and **Dr. Santosh Herur**, Professor & Head, Department of E&CE. Faculty members and students were also present. The program commenced with an invocation song by Ms. Anusha and team, followed by the inauguration ceremony where the dignitaries symbolically watered a plant, marking the beginning of the event. A warm welcome speech was delivered by Ms. Harshitha V. K.

Dr. Ganesh D. B., Principal & Director, encouraged students to believe in themselves and emphasized

the importance of balancing academics and work life. **Dr. Santosh Herur** Professor & Head, Department of E&CE shared valuable insights about the current job market and extended his best wishes to the outgoing students.

Ms. Kruthika G. P. introduced the Chief Guest. **Mr. Mallesh V. S.**, who delivered an insightful speech focusing on current industry trends and career opportunities. He elaborated on emerging technologies such as automation, augmentation, and Artificial Intelligence (AI), explaining their growing influence across various domains. He highlighted how AI is revolutionizing even core sectors and encouraged students to focus on AI-enabled learning, practical exposure, and staying updated with industry developments. He concluded by wishing the students success in their future endeavors.

Prof. Sharanabasaveshwara H. B., Forum Coordinator, provided an overview of the diverse activities conducted by the forum throughout the academic year. Academic toppers of the odd semester (2024–2025) were awarded for their excellence and dedication. Students who excelled in various departmental events were also recognized, promoting a spirit of achievement and participation. The outgoing students shared their feedback and reflections, and the event concluded with a vote of thanks delivered by Ms. Srigowri G. B.

Student's Achievement : 8th Semester Results



Priya R
(4JD21EC035)
98.5%



Bhumika S
(4JD21EC011)
98%



Sindhushree M J
(4JD21EC048)
97.75%

VTU 8th Semester CBCS Results for May 2025 Examinations have been released for all regions on 30th May 2025. Department of Electronics & Communication Engineering has achieved a remarkable passing percentage of **100%** in the 8th semester results. **Priya R** has secured 1st place with 98.5%, **Bhumika S** has secured 2nd place with 98% and **Sindhushree M J** has secured 3rd place with 97.75%. Hearty Congratulations to all the toppers of E & C E Department on behalf of College Management, Principal, HOD, Teaching and Non-teaching staff members.